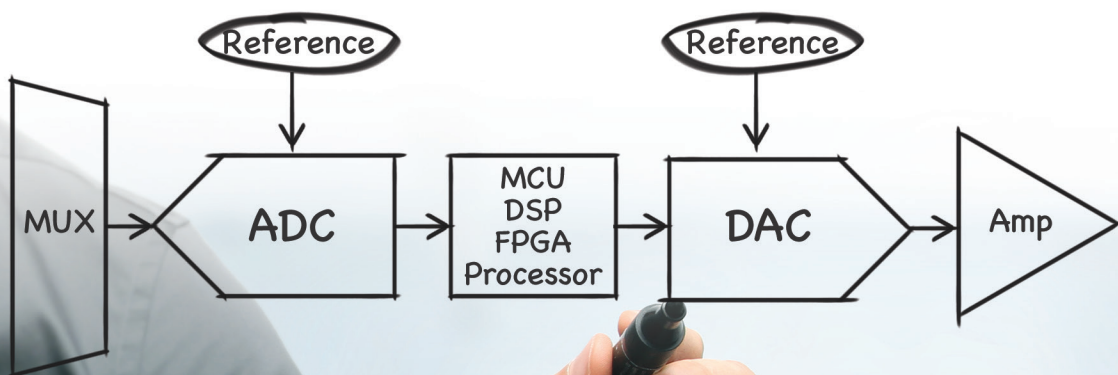


# Precision Data Converter

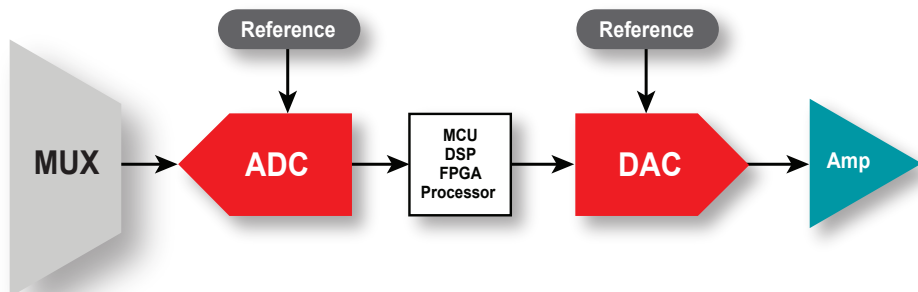
## Companion Devices Selection Guide





# Precision Data Converter

## Companion Devices Selection Guide



Revision: October 2018

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# Introduction

In this guide you will find tables organized by the data converter listed in the left most column. Each row contains a list of devices which when paired with the data converter can be used to achieve the signal chain performance and optimization specified.

## Table Version 1: SAR ADCs

SAR ADC	ADC Input Driver		
	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS9110</b> 2 MSPS, 1 channel, fully differential	OPA2625	1) 2-MSPS throughput 2) Fully differential input 3) 5-V single supply	<b>Optimized for linearity:</b> +/-0.5 LSB INL (TYP), +/-1.5 LSB INL (MAX)

ADC/DAC and its highlights

Recommended data converter input driver, selected to achieve optimum performance (e.g. linearity) and the tested system performance

## Table Version 2: Delta-Sigma ADCs and DACs

Precision digital-to-analog converters					Product type
Product number	Product description	Resolution (bits)	No. of channels	Output type	
<b>DAC9881</b>	18-bit, 1-channel, low noise, buffered voltage output DAC	18	1	Buffered voltage	Op amp
					Signal switch

ADC/DAC and its highlights

In the right most column you will find a reference design developed and rigorously tested by signal chain experts. They include the following information, which can be used to decrease system development time and increase system performance:

- Circuit Topology
- PCB Layout
- Simulated Results
- Component Selection
- Calculated Results
- Measured Results

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA2625, OPA378	Composite zero-offset reference buffer	TI Design: TIPD115

Recommended reference and reference driver for the ADC, chosen to achieve the previously specified system performance

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
OPA2192	36-V, precision, RRIO, low offset voltage, low input bias current op amp with e-trim	DAC sample and hold glitch reduction reference design	TIPD142
TS12A4515	Low-voltage, low on-state resistance SPST CMOS analog switches		

Companion products that pair well the ADC/DAC highlighted. These companion devices are also featured in the reference design to achieve an optimally performing system

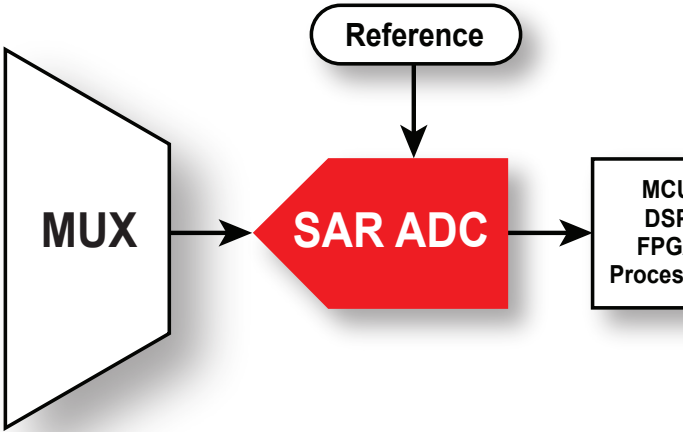


# Contents

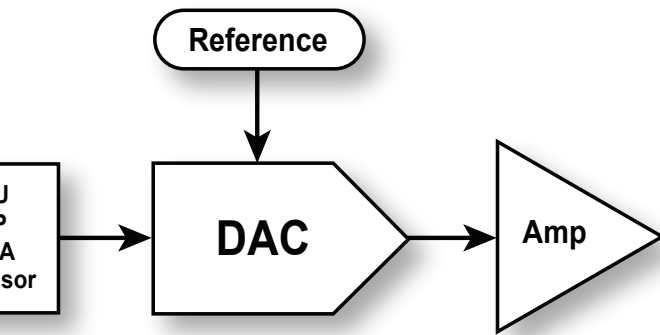
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<b>Delta-Sigma ADC.....</b>	<b>46</b>
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# SAR ADC







# SAR ADC

Device	Resolution (bits)	Sample rate (kSPS)	No. of channels	Input range (V)	SNR (dB, typ)	THD (dB, typ)	INL ( $\pm$ LSB, Max)	Operating temperature range range ( $^{\circ}$ C)
<b>ADS9110</b>	18	2000	1	$\pm$ 5.1	100	-118	1.5	-40 to 85
<b>ADS8881</b>	18	1000	1	$\pm$ 5	100	-115	2	-40 to 85, 0 to 70
<b>ADS8887</b>	18	100	1	$\pm$ 5	100	-115	3	1.5
<b>ADS8885</b>	18	400	1	0 to 5	100	-115	3	-40 to 85
<b>ADS8883</b>	18	680	1	$\pm$ 5	100	-115	3	-40 to 85
<b>ADS8383</b>	18	500	1	0 to 4.096	88	-112	4	-40 to 85
<b>ADS8380</b>	18	600	1	0 to 4.096	91	-112	4	-40 to 85
<b>ADS8381</b>	18	580	1	0 to 4.096	88	-112	4	-40 to 85
<b>ADS8382</b>	18	600	1	0 to 4.096	96	-116	3	-40 to 85
<b>ADS8484</b>	18	1250	1	0 to 4.096	98.5	-120	2.5	-40 to 85
<b>ADS8482</b>	18	1000	1	0 to 4.096	99	-121	2.5	-40 to 85
<b>ADS8481</b>	18	1000	1	0 to 4.096	94	-112	3.5	-40 to 85
<b>ADS8866</b>	16	100	1	0 to 5	93	-108	2	-40 to 85
<b>ADS8867</b>	16	100	1	$\pm$ 5	96.5	-112	1	-40 to 85
<b>ADS8864</b>	16	400	1	0 to 5	93	-108	2	-40 to 85

Device	Resolution (bits)	Sample rate (kSPS)	No. of channels	Input range (V)	SNR (dB, typ)	THD (dB, typ)	INL ( $\pm$ LSB, Max)	Operating temperature range range ( $^{\circ}$ C)
<b>ADS8865</b>	16	400	1	$\pm$ 5	96.5	-112	1	-40 to 85
<b>ADS8342</b>	16	250	4	$\pm$ 2.5	84.6	-89	4	-40 to 85
<b>ADS8344</b>	16	100	8	0 to 5.25	86	-90	6	-40 to 85
<b>ADS8345</b>	16	100	8	0 to 5.25	85	-96	6	-40 to 85
<b>ADS8863</b>	16	680	1	$\pm$ 5	96.5	-112	1	-40 to 85
<b>ADS8862</b>	16	680	1	0 to 5	93	-108	2	-40 to 85
<b>ADS8860</b>	16	1000	1	0 to 5	93	-108	2	-40 to 85
<b>ADS8861</b>	16	1000	1	$\pm$ 5	96.5	-112	1	-40 to 85
<b>ADS8317</b>	16	250	1	0 to 5.5	90	-106	1.5	-40 to 85
<b>ADS8339</b>	16	250	1	0 to 5.5	93.9	-111	2	-40 to 85
<b>ADS8332</b>	16	500	8	0 to 5.5	91.5	-100	2	-40 to 85
<b>ADS8331</b>	16	500	4	0 to 5.5	91.5	-100	2	-40 to 85
<b>ADS8350</b>	16	750	2	0 to 5.5	85	-96	2.5	-40 to 125
<b>ADS8353</b>	16	600	2	0 to 5.5	89	-100	2.5	-40 to 125
<b>ADS8354</b>	16	700	2	0 to 5.5	93	-100	2.5	-40 to 125

# SAR ADC

Device	Resolution (bits)	Sample rate (kSPS)	No. of channels	Input range (V)	SNR (dB, typ)	THD (dB, typ)	INL ( $\pm$ LSB, Max)	Operating temperature range range ( $^{\circ}$ C)
<b>ADS8688</b>	16	500	8	$\pm$ 10.24	92	-102	2	-40 to 125
<b>ADS8684A</b>	16	500	4	$\pm$ 10.24	92	-102	2	-40 to 125
<b>ADS8688A</b>	16	500	8	$\pm$ 10.24	92	-102	2	-40 to 125
<b>ADS8684</b>	16	500	4	$\pm$ 10.24	92	-102	2	-40 to 125
<b>ADS8422</b>	16	400	1	0 to 4.096	93	-114	2	-40 to 85
<b>ADS8505</b>	16	250	1	$\pm$ 10	88	-105	1.5	-40 to 85
<b>ADS7945</b>	14	2000	2	0 to 5.25	84	-92	1.5	-40 to 125
<b>ADS7946</b>	14	2000	2	0 to 5.25	82	-85	2.5	-40 to 125
<b>ADS7850</b>	14	750	2	0 to 5.5	81.5	-90	1.5	-40 to 125
<b>ADS7891</b>	14	3000	1	0 to 2.5	78.5	-93	1.5	-40 to 85
<b>ADS7853</b>	14	1000	2	0 to 5.5	84	-100	2	-40 to 125
<b>ADS7854</b>	14	1000	2	0 to 5.5	85	-98	1.5	-40 to 125
<b>ADS7851</b>	14	1500	2	0 to 5.25	83.5	-90	2	-40 to 125
<b>ADS8674</b>	14	500	4	$\pm$ 10.24	85	-100	0.75	-40 to 125

Device	Resolution (bits)	Sample rate (kSPS)	No. of channels	Input range (V)	SNR (dB, typ)	THD (dB, typ)	INL ( $\pm$ LSB, Max)	Operating temperature range range ( $^{\circ}$ C)
<b>ADS8678</b>	14	500	8	$\pm$ 10.24	85	-100	0.75	-40 to 125
<b>ADS7042</b>	12	1000	1	0 to 3.6	70	-80	1	-40 to 125
<b>ADS7043</b>	12	1000	1	0 to 3.6	70	-80	1	-40 to 125
<b>ADS7044</b>	12	1000	1	0 to 3.6	71	-85	1	-40 to 125
<b>TLV2553</b>	12	200	11	0 to 5.5	n/a	n/a	1	-40 to 85
<b>ADS7952</b>	12	1000	12	0 to 5.25	71.7	-82	1	-40 to 125
<b>ADS7950</b>	12	1000	4	0 to 5.25	71.7	-82	1	-40 to 125
<b>ADS7951</b>	12	1000	8	0 to 5.25	71.7	-82	1	-40 to 125
<b>ADS7953</b>	12	1000	16	0 to 5.25	71.7	-82	1	-40 to 125
<b>ADS8638</b>	12	1000	8	$\pm$ 10	71.8	-81	1.5	-40 to 125
<b>ADS7250</b>	12	750	2	0 to 5.5	73	-90	1	-40 to 125
<b>ADS7254</b>	12	1000	2	0 to 5.5	74	-92	1	-40 to 125
<b>ADS7253</b>	12	1000	2	0 to 5.5	73.5	-90	1	-40 to 125
<b>ADS7251</b>	12	2000	2	0 to 5.25	73	-90	1	-40 to 125

# Resolution: 20 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8900B</b> 1 MSPS, 1 channel, $\pm 5$ input range, int reference buffer	OPA2625	1) 1-MSPS throughput 2) $\pm 1$ ppm INL 3) 102 dB SNR 4) -125 dB THD	<b>Optimized for AC and DC performance:</b> $\pm 0.75$ ppm INL, 102.5 dB SNR, -125 dB THD
	THS4551	1) 1-MSPS throughput 2) $5.7 \text{ kV}_{\text{RMS}}$ Isolation 3) 101 dB SNR 4) -125 dB THD	<b>Jitter optimized with digital isolation:</b> 100.9 dB SNR, -127.7 dB THD
	THS4551	1) 1-MSPS throughput 2) $5.7 \text{ kV}_{\text{RMS}}$ Isolation 3) 101 dB SNR 4) -125 dB THD	<b>Optimized for AC distortion with isolation:</b> 101.1 dB SNR, -122.6 dB THD

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5050	$V_{REF}=5.0\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C		Internal reference buffer	TI Design: TIPD211
REF5050	$V_{REF}=5.0\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C		Internal reference buffer	TI Design: TIDA-01035
REF5050	$V_{REF}=5.0\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C		Internal reference buffer	TI Design: TIDA-01037

# Resolution: 18 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8910B</b> 1 MSPS, 1 channel, ±5 input range, int reference buffer	OPA2625	1) 1-MSPS throughput 2) ±0.5 LSB INL 3) 101 db SNR 4) -125 db THD	<b>Optimized for AC and DC performance:</b> ±0.3 LSB INL, 101.2 dB SNR, -125 dB THD
<b>ADS9110</b> 2 MSPS, 1 channel, fully differential	OPA2625	1) 2-MSPS throughput 2) Fully differential input 3) 5-V single supply	<b>Optimized for linearity:</b> +/-0.5 LSB INL (TYP), +/-1.5 LSB INL (MAX)
<b>ADS8881</b> 1 MSPS, 1 channel, ±5 input range, fully differential	OPA350	1) 1-MSPS throughput 2) Transient step input 3) 5-V single supply	<b>18-bit, full-scale, step settling at 1-MSPS throughput:</b> Settling time <500 ns, INL ±1.5 LSB, power < 70 mW
	OPA320	1) 1-MSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR >98 dB, THD <-105 dB
	THS4521		<b>Optimized for lowest distortion and noise:</b> THD <-110dB, SNR >98 dB, P=<40 mW
	THS4521	1) 1-MSPS throughput 2) Fully differential input 3) Support for bipolar inputs	<b>Optimized for low total harmonic distortion:</b> THD <-110dB, SNR >98 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> Input noise=48 uVRMS, SNR >90 dB, P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> System noise=37.56 µVRMS, effective resolution >17.0-bit, P=<1 mW
<b>ADS8883</b> 680 kSPS, 1 channel, ±5 input range, fully differential	OPA350	1) 680-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>18-bit full scale step settling at 680-kSPS throughput:</b> Settling time <1500ns, INL ±1.5LSB, power < 70 mW
	OPA320	1) 680-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR >98 dB, THD <-105 dB
	THS4521		<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >98dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> Input noise=48 uVRMS, SNR >90 dB, P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> System noise=37.56 uVRMS, effective resolution >17.0-bit, P=<1 mW



ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5050	$V_{REF}=5.0\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Internal reference buffer		TI Design: TIPD211
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA2625, OPA378	Composite zero-offset reference buffer	TI Design: TIPD115
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: TIPD112
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: TIPD113
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: TIPD115
REF6025	$V_{REF}=2.5\text{-V}$ , 0.05% init. accuracy, low drift 5 ppm/°C integrated high-bandwidth buffer	Integrated into: REF6025 voltage reference	Accounted for in REF6025 voltage reference specifications	REF6025EVM-PDK
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	TI Design: TIPD116
REF3325	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	TI Design: TIPD114
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: TIPD112
REF5045	$zV_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: TIPD113
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: TIPD115
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ . Data rate=10-kSPS	TI Design: TIPD116
REF3325	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	TI Design: TIPD114

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 18 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8885</b> 1 channel, ±5 input range, fully differential	OPA350	1) 400-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>18-bit full scale step settling at 400-kSPS throughput:</b> Settling time <2500 ns, INL ±1.5 LSB, Power < 70 mW
	OPA320	1) 400-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR >98 dB, THD <-105 dB
	THS4521		<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >98 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> Input noise=48 µVRMS, SNR >98 dB, P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> Syst noise=37.56 µVRMS, effective res >17.0-bit, P=<1 mW
<b>ADS8887</b> 100 kSPS, 1 channel, ±5 input range, fully differential	OPA363	1) 1-MSPS throughput 2) Transient step input 3) 5-V single supply	<b>18-bit full scale step settling at 100-kSPS throughput:</b> Settling time <10µs
	OPA313	1) 100-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35mW, SNR >98 dB, THD <-105 dB
	THS4521		<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >98 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> Input noise=48 µVRMS, SNR >90 dB, P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> Syst noise=37.56 µVRMS, effective res >17.0-bit, P=<1 mW
<b>ADS8380</b> 600 kSPS, 1 channel, 0 - 4.096 input range, single ended	THS4031	1) Max throughput 2) AC input specified on the datasheet 3) ±15V dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333+THS4281</a>	Low offset , low offset drift 0.05μV/°C, wide BW, low output impedance	TI Design: <a href="#">TIPD112</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333+THS4281</a>	Low offset , low offset drift 0.05μV/°C, wide BW, low output impedance	TI Design: <a href="#">TIPD113</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333+THS4281</a>	Low offset , low offset drift 0.05μV/°C, wide BW, low output impedance	TI Design: <a href="#">TIPD115</a>
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	<a href="#">OPA313</a>	Micro power, low drift 2μV/°C Data rate=10-kSPS	TI Design: <a href="#">TIPD116</a>
REF3325	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	<a href="#">OPA313</a>	Micro power, low drift 2μV/°C Data rate=10-kSPS	TI Design: <a href="#">TIPD114</a>
REF5045	$V_{REF}=4.5\text{ V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333+THS4281</a>	Low offset , low offset drift 0.05μV/°C, wide BW, low output impedance	TI Design: <a href="#">TIPD112</a>
REF5045	$V_{REF}=4.5\text{ V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333+THS4281</a>	Low offset , low offset drift 0.05μV/°C, wide BW, low output impedance	TI Design: <a href="#">TIPD113</a>
REF5045	$V_{REF}=4.5\text{ V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333+THS4281</a>	Low offset , low offset drift 0.05μV/°C, wide BW, low output impedance	TI Design: <a href="#">TIPD115</a>
REF3030	$V_{REF}=3.0\text{ V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	<a href="#">OPA313</a>	Micro power, low drift 2μV/°C Data rate=10-kSPS	TI Design: <a href="#">TIPD116</a>
REF3325	$V_{REF}=2.5\text{ V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	<a href="#">OPA313</a>	Micro power, low drift 2μV/°C Data rate=10-kSPS	TI Design: <a href="#">TIPD114</a>
1) Internal reference can be used 2) External reference mode		Internal reference buffer		<a href="#">ADS8380EVM</a>

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 18 bit

	ADC Input Driver		
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8381</b> 580 kSPS, 1 channel, 0 - 4.096 input range, single ended	THS4031	1) Max throughput 2) AC input specified on the datasheet 3) $\pm 15V$ dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec
<b>ADS8382</b> 600 kSPS, 1 channel, $\pm 4.096$ input range, fully differential	THS4131	1) Max throughput 2) AC input specified on the datasheet 3) $\pm 15V$ dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec
<b>ADS8383</b> 500 kSPS, 1 channel, 0 - 4.096 input range, single ended	THS4031	1) Max throughput 2) AC input specified on the datasheet 3) $\pm 15V$ dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec
<b>ADS8481</b> 1 MSPS, 1 channel, 0 - 4.096 input range, single ended	THS4031, THS4032	1) Max throughput 2) AC input specified on the datasheet 3) $\pm 15V$ dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec
<b>ADS8482</b> 1 MSPS, 1 channel, $\pm 4.096$ input range, fully differential	THS4031, THS4032	1) Max throughput 2) AC input specified on the datasheet 3) $\pm 15V$ dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec
<b>ADS8484</b> 1.25 MSPS, 1 channel, $\pm 4.096$ input range, fully differential	THS4031, THS4032	1) Max throughput 2) AC input specified on the datasheet 3) $\pm 15V$ dual supply	<b>Optimized for dynamic performance:</b> refer to datasheet for performance spec

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
	1) Internal reference can be used 2) External reference mode		Internal reference buffer	<a href="#">ADS8381EVM</a>
	1) Internal reference can be used 2) External reference mode		Internal reference buffer	<a href="#">ADS8382EVM</a>
	1) Internal reference can be used 2) External reference mode		Internal reference buffer	<a href="#">ADS8383EVM</a>
	1) Internal reference can be used 2) External reference mode		Internal reference buffer	<a href="#">ADS8481EVM</a>
	1) Internal reference can be used 2) External reference mode		Internal reference buffer	<a href="#">ADS8482EVM</a>
	1) Internal reference can be used 2) External reference mode		Internal reference buffer	<a href="#">ADS8484EVM</a>

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8920B</b> 1 MSPS, 1 channel, ±5 input range, int reference buffer	OPA2625	1) 1-MSPS throughput 2) ±0.5 ppm INL 3) 95 db SNR 4) 125 db THD	<b>Optimized for AC and DC performance:</b> ±0.2 LSB INL, 96.5 dB SNR, -125 dB THD
<b>ADS8681</b> 1 MSPS, 1 channel, ±12 input range, bipolar & unipolar	Integrated Front End	1) Throughput of 1-MSPS 2) AC input specified on the datasheet 3) 5-V single supply	<b>16-bit 1-MSPS data acquisition reference design, isolated for high-voltage common-mode rejection</b>
<b>ADS8861</b> 1 MSPS, 1 channel, ±5 input range, fully differential	OPA350	1) 1-MSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 1-MSPS throughput:</b> Settling time <500ns, power < 70 mW
	OPA320	1) 1-MSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR> 96 dB, THD <-105 dB
	THS4521	1) 1-MSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >96 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC Signals, ultra low power:</b> P=<1 mW
	THS4031	1) 1-MSPS throughput 2) 10-kHz AC input 3) ±15V dual supply	<b>Optimized for low distortion:</b> 100 kHz signal: THD <-110 dB
<b>ADS8863</b> 680 kSPS, 1 channel, ±5 input range, fully differential	OPA363	1) 680-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 680-kSPS throughput:</b> Settling time <1500ns, power < 70 mW
	OPA313	1) 10-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR>96 dB, THD <-105 dB
	THS4521	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >96 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> P=<1 mW

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5050	$V_{REF}=5.0\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Internal reference buffer		TI Design: TIPD211
	Internal reference	Internal reference buffer	Low output impedance, drift 4ppm/°C	TI Design: TIDA106
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333, THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	TI Design: TIPD112
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	TI Design: TIPD113
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333+THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	TI Design: TIPD115
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/\text{C}$ Data rate=10-kSPS	TI Design: TIPD116
REF3425	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/\text{C}$ Data rate=10-kSPS	TI Design: TIPD114
REF5040	$V_{REF}=4.0\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA188+THS4031	Low offset, low offset drift 0.03 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	Contact E2E
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333, THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	TI Design: TIPD112
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333, THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	TI Design: TIPD113
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: OPA333, THS4281	Low offset, low offset drift 0.05 $\mu\text{V}/\text{C}$ , wide BW, low output impedance	TI Design: TIPD115
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/\text{C}$ . Data rate=10-kSPS	TI Design: TIPD116
REF3325	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	OPA313	Micro power, low drift 2 $\mu\text{V}/\text{C}$ . Data rate=10-kSPS	TI Design: TIPD114

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8865</b> 400 kSPS, 1 channel, ±5 input range, fully differential	OPA350	1) 400-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 400-kSPS throughput:</b> Settling time <2500 ns, power < 70 mW
	OPA320	1) 400-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR>98 dB, THD <-105 dB
	THS4521		<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >98 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> P=<1 mW
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC Signals, ultra low power:</b> P=<1 mW
<b>ADS8867</b> 100 kSPS, 1 channel, ±5 input range, fully differential	OPA363	1) 100-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 680-kSPS throughput:</b> Settling time <10 us
	OPA313	1) 100-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest power:</b> P<35 mW, SNR>96 dB, THD <-105 dB
	THS4521		<b>Optimized for lowest distortion and noise:</b> THD <-110 dB, SNR >96 dB, P=<40 mW
	OPA333	1) 10-kSPS throughput 2) 200-Hz input (ECG signals) 3) 3.3-V single supply	<b>ECG signals, ultra low power:</b> P=<1 mW
		1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> P=<1 mW



ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD112</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD113</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD115</a>
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	<a href="#">OPA313</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	TI Design: <a href="#">TIPD116</a>
REF3325	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	<a href="#">OPA313</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	TI Design: <a href="#">TIPD114</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD112</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD113</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD115</a>
REF3030	$V_{REF}=3.0\text{-V}$ , 0.2% init. accuracy, 50 ppm/°C (0 to +70°C)	<a href="#">OPA313</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ . Data rate=10-kSPS	TI Design: <a href="#">TIPD116</a>
REF3325	$V_{REF}=2.5\text{-V}$ , 0.15% init. accuracy, 30 ppm/°C (-40 to +125°C)	<a href="#">OPA313</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ . Data rate=10-kSPS	TI Design: <a href="#">TIPD114</a>

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8860</b> 1 MSPS, 1 channel, 0 - 5 input range, pseudo differential	OPA836	1) 1-MSPS throughput 2) 10-kHz sine wave input 3) 5-V single supply	<b>Optimized for lowest distortion and noise:</b> SNR>91 dB, THD < -108 dB, P=30.75 mW
	OPA320	1) 1-MSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 1 MSPS throughput:</b> Settling time <900 ns, INL ±0.5 LSB, power < 25 mW
	OPA625	1) 1-MSPS throughput 2) DC input 3) 5-V single supply	<b>16-bit, muxed CH</b> Settling time <500 ns
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> P<300 uW
<b>ADS8862</b> 680 kSPS, 1 channel, 0 - 5 input range, pseudo differential	OPA836	1) 680-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Optimized for lowest distortion and noise:</b> SNR>91 dB, THD=-110 dB, P=30.75 mW
	OPA320	1) 680-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 1-MSPS throughput:</b> Settling time <1500 ns, INL ±0.5LSB, power < 25 mW
	OPA625	1) 680-kSPS throughput 2) DC input 3) 5-V single supply	<b>16-bit, muxed CH</b> Settling time <1500 ns
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> P<300 uW
<b>ADS8864</b> 400 kSPS, 1 channel, 0 - 5 input range, pseudo differential	OPA836	1) 400-kSPS throughput 2) 10-kHz sine wave input 3) 5-V single supply	<b>Optimized for lowest distortion and noise:</b> SNR>91 dB, THD=-110 dB, P=30.75 mW
	OPA320	1) 400-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 1-MSPS throughput:</b> Settling time <2500 ns, INL ±0.5LSB, power < 25 mW
	OPA625	1) 400-kSPS throughput 2) DC signals 3) 5-V single supply	<b>16-bit, muxed CH</b> Settling time <2500 ns
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> P<300 uW
	OPA192, OPA140	1) 400-kSPS throughput 2) 10-kHz AC input 3) ±15V dual supply	<b>16-bit,4-CH muxed for high voltage diff inputs:</b> P<<1 mW

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	<a href="#">TIDU014</a>
REF6041	$V_{REF}=4.096\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD173</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD169</a>
REF3330	Low power, 0.15% init. accuracy, 30 ppm/°C @3.9 $\mu\text{A}$ (-40°C to +125°C)	<a href="#">OPA333</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	<a href="#">ADS8860 data-sheet</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	<a href="#">TIDU014</a>
REF5040	$V_{REF}=4.096\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD173</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD169</a>
REF3330	Low power, 0.15% init. accuracy, 30 ppm/°C @3.9 $\mu\text{A}$ (-40°C to +125°C)	<a href="#">OPA333</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	<a href="#">ADS8860 data-sheet</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	<a href="#">TIDU014</a>
REF5040	$V_{REF}=4.096\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD173</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD169</a>
REF3330	Low power, 0.15% init. accuracy, 30 ppm/°C @3.9 $\mu\text{A}$ (-40°C to +125°C)	<a href="#">OPA333</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	<a href="#">ADS8860 data-sheet</a>
REF3440	$V_{REF}=4.096\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	<a href="#">OPA350</a>	Wide BW, low output impedance, drift 4 $\mu\text{V}/^\circ\text{C}$	TI Design: <a href="#">TIPD151</a>

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8866</b> 100 kSPS, 1 channel, 0 - 5 input range, pseudo differential	OPA836	1) 100-kSPS throughput 2) 10-kHz sine wave input 3) 5-V single supply	<b>Optimized for lowest distortion and noise:</b> SNR>91 dB, THD=-110 dB, P=30.75 mW
	OPA364	1) 100-kSPS throughput 2) Transient step input 3) 5-V single supply	<b>16-bit full scale step settling at 100-kSPS throughput:</b> Settling time <10 $\mu$ s, INL $\pm$ 0.5LSB, power < 25 mW
	OPA625	1) 100-kSPS throughput 2) DC signals 3) 5-V single supply	<b>16-bit, muxed CH</b> Settling time <10 $\mu$ s
	OPA333	1) 10-kSPS throughput 2) DC signals 3) 3.3-V single supply	<b>DC signals, ultra low power:</b> P<300 $\mu$ W
<b>ADS8350</b> 750 kSPS, 2 channels, 0 - 5 input range, pseudo differential	OPA836	1) 750-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>85 dB@10kHz, THD<-94 dB@10 kHz
	THS4032	1) 750-kSPS throughput 2) 100-kHz AC input 3) $\pm$ 15V dual supply	<b>Dual-channel, simultaneous sampling:</b> SNR>85 dB@100kHz, THD<-91 dB@100 kHz
<b>ADS8353</b> 600 kSPS, 2 channels, 0 - 5 input range, single ended pseudo differential	OPA836	1) 600-kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>83 dB@100 kHz, THD<-95 dB@100 kHz
	THS4032	1) 600-kSPS throughput 2) 100-kHz AC input 3) $\pm$ 15V dual supply	<b>Dual-channel, simultaneous sampling:</b> SNR>83 dB@10 kHz, THD<-100 dB@10 kHz
<b>ADS8354</b> 700 kSPS, 2 channels, $\pm$ 5 input range, fully differential	THS4521	1) 700-kSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>88 dB@10 kHz, THD<-100 dB@10 kHz
	THS4531A	1) 700-kSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Interface to sin/cos encoders with high-resolution position interpolation</b>
	THS4531A	1) 700-kSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Interface to sin/cos encoder with Sitara AM437x</b>

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> , <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	<a href="#">TIDU014</a>
REF3440	$V_{REF}=4.096\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> + <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD173</a>
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Composite amplifier: <a href="#">OPA333</a> + <a href="#">THS4281</a>	Low offset, low offset drift 0.05 $\mu\text{V}/^\circ\text{C}$ , wide BW, low output impedance	TI Design: <a href="#">TIPD169</a>
REF3330	Low power, 0.15% init. accuracy, 30 ppm/°C @3.9 $\mu\text{A}$ (-40°C to +125°C)	<a href="#">OPA333</a>	Micro power, low drift 2 $\mu\text{V}/^\circ\text{C}$ Data rate=10-kSPS	<a href="#">ADS8860 data-sheet</a>
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	<a href="#">OPA2350</a>	Dual channel, wide BW, low output impedance, drift 4ppm/°C	<a href="#">ADS8350EVM-PDK</a>
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	<a href="#">OPA2350</a>	Dual channel, wide BW, low output impedance, drift 4ppm/°C	<a href="#">ADS8350 data-sheet</a>
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	<a href="#">OPA2350</a>	Dual channel, wide BW, low output impedance, drift 4ppm/°C	<a href="#">ADS8353EVM-PDK</a>
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	<a href="#">OPA2350</a>	Dual channel, wide BW, low output impedance, drift 4ppm/°C	<a href="#">ADS8353 data-sheet</a>
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	<a href="#">OPA2350</a>	Dual channel, wide BW, low output impedance, drift 4ppm/°C	TI Design: <a href="#">TIDA-00499</a>
Int ref / ext ref <a href="#">REF2033</a>	REF2033: 2 Outputs, VREF and VREF / 2, 8 ppm/°C drift from -40°C to 125°C	<a href="#">OPA2365</a>	Low-noise, single-supply, rail-to-rail op amp	TI Design: <a href="#">TIDA00176</a>
Int ref / ext ref <a href="#">REF2025</a>	REF2025 low-drift, low-power, dual-output VREF and VREF/2 voltage reference	Internal buffer	Internal buffer	TI Design: <a href="#">TIDA-00178</a>

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

	ADC Input Driver		
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8331</b> 500 kSPS, 4 channels, 0 - 4.096 input range, single ended	OPA211	1) Max throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8332</b> 500 kSPS, 8 channels, 0 - 4.096 input range, single ended			
<b>ADS8339</b> 250 kSPS, 1 channel, 0 - 5 input range, single ended	OPA836	1) Max throughput 2) AC input specified on datasheet 3) 5-V single supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8342</b> 250 kSPS, 4 channels, $\pm 2.5$ input range, single ended bipolar	OPA725 OPA726	1) Max throughput 2) AC input specified on datasheet 3) $\pm 5$ -V dual supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8344</b> 100 kSPS, 8 channels, 0 - 5 input range, single ended	OPA132	1) Max throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8345</b> 100 kSPS, 8 channels, $\pm 2.5$ input range, fully differential			
<b>ADS8422</b> 4 MSPS, 1 channel, $\pm 4.096$ input range, fully differential	THS4131 THS4031 THS4032	1) Max throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5040	$V_{REF}=4.096\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	OPA350	Wide BW, low output impedance, drift 4ppm/°C	ADS8331EVM
				ADS8332EVM
REF5045	$V_{REF}=4.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	OPA333+THS4281	Low offset, low offset drift 4 $\mu\text{V}/\text{°C}$ , wide BW, low output impedance	ADS8339EVM-PDK
REF5025	$V_{REF}=2.5\text{-V}$ , 0.05% init. accuracy, low drift 3 ppm/°C	Internal reference buffer		ADS8342EVM
REF3440	$V_{REF}=4.096\text{-V}$ , 0.2% init. accuracy, low drift 4 ppm/°C	OPA364	Wider BW, low offset, low drift 3 $\mu\text{V}/\text{°C}$	ADS8344EVM
				ADS8345EVM
Internal reference	Int Ref 4.096-V with low drift 6ppm/°C	Internal reference buffer		Contact E2E

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8684</b> 500 kSPS, 4 channels, single ended	OPA2209	Amplifier only for driving aux channel integrated AFE for input channels  1) Max specified throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8688</b> 500 kSPS, 8 channels, single ended  input ranges: $\pm 10.24$ $\pm 5.12$ $\pm 2.56$ 0 - 10.24 0 - 5.12			
<b>ADS8684A</b> 500 kSPS, 4 channels, single ended	OPA2209	Amplifier only for driving aux channel integrated AFE for input channels  1) Max specified throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8688A</b> 500 kSPS, 8 channels, single ended  input ranges: $\pm 10.24$ $\pm 5.12$ $\pm 2.56$ 0 - 10.24 0 - 5.12 0 - 2.56 0 - 1.28			
<b>ADS8317</b> 250 kSPS, 1 channel, $\pm 2.5$ input range, fully differential	OPA2376	1) Max throughput 2) 10-kHz sine wave input 3) 5-V single supply	<b>Optimized for dynamic performance:</b> ENOB=14.5



ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5040	<p>1) Internal reference: 4.096V, drift 10ppm/°C max.</p> <p>2) External reference: REF5040 4.096-V, 0.05% init. accuracy, low drift 3ppm/°C</p>	Internal reference buffer		ADS8688EVM-PDK
REF5040	<p>1) Internal reference: 4.096V, drift 10ppm/°C max.</p> <p>2) External reference: REF5040 4.096-V, 0.05% init. accuracy, low drift 3ppm/°C</p>	Internal reference buffer		ADS8688AEVM-PDK
REF5025	$V_{REF} = 2.5\text{-V}$ , 0.05% init. accuracy, low drift 3ppm/°C	OPA376	Low power, low drift 0.26 $\mu\text{V}/^\circ\text{C}$ @750 $\mu\text{A}$	Contact E2E

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 16 bit

	ADC Input Driver		
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS8505</b> 250 kSPS, 1 channel, ±10 input range, single ended	OPA140	1) Max throughput 2) 10-kHz sine wave input 3) ±15-V dual supply	<b>Optimized for dynamic performance:</b> ENOB=14.5

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5025	1) Internal reference: 2.5-V, drift 8ppm/°C typ  2) External reference: REF5025 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	Internal reference buffer		ADS8505EVM

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 14 bit

		ADC Input Driver	
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS7853</b> 1 MSPS, 2 channels, 0 - 5 input range, single ended/pseudo differential	OPA836	1) 1-MSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>81 dB@10 kHz, THD<-95 dB@10 kHz
	THS4032	1) 1-MSPS throughput 2) 100-kHz AC input 3) $\pm 15$ -V dual supply	<b>Dual-channel, simultaneous sampling:</b> SNR>78.5 dB@100 kHz, THD<-88 dB@100 kHz
<b>ADS7854</b> 1 MSPS, 2 channels, $\pm 5$ input range, fully differential	THS4521	1) 1-MSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>81.5 dB@10 kHz, THD<-90 dB@10 kHz
	THS4531	1) 1-MSPS throughput 2) 20-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling for optical encoders:</b> SNR>83.3 dB@2 kHz, THD<-88 dB@2 kHz
<b>ADS7850</b> 750 kSPS, 2 channels, 0 - 5 input range, pseudo differential	OPA836	1) 750 kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>81.5 dB@10 kHz, THD<-90 dB@10 kHz
	THS4032	1) 750 kSPS throughput 2) 100-kHz AC input 3) $\pm 15$ -V dual supply	<b>Dual-channel, simultaneous sampling:</b> SNR>81.5 dB@100 kHz, THD<-90 dB@100 kHz
<b>ADS7851</b> 1.5 MSPS, 2 channels, $\pm 5$ input range, fully differential	THS4521	1) 1-MSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>82.3 dB@100 kHz, THD<-93 dB@100 kHz
			<b>Dual-channel, simultaneous sampling for optical encoders</b>
<b>ADS7945</b> 2 MSPS, 2 channels, $\pm 5$ input range, fully differential	OPA836	1) Max throughput 2) AC input specified on datasheet 3) 5-V single supply	<b>microPower, differential SAR ADC:</b> Refer to datasheet for performance spec
<b>ADS7946</b> 2 MSPS, 2 channels, 0 - 5 input range, single ended	OPA836	1) Max throughput 2) AC input specified on datasheet 3) 5-V single supply	<b>microPower, differential SAR ADC:</b> Refer to datasheet for performance spec

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4 μV/°C	ADS7853EVM-PDK
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4 μV/°C	TI Design: TIDA-00208
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4 μV/°C	TI Design: TIDA-00201
Internal reference	Int $V_{REF}=2.5\text{-V}, \pm 10\text{ppm}/^{\circ}\text{C}$	N/A	N/A	TI Design: TIPD117
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4ppm/°C	ADS8350EVM-PDK
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4ppm/°C	ADS8350 data-sheet
Internal reference	Int $V_{REF}=2.5\text{-V}, \pm 10\text{ppm}/^{\circ}\text{C}$	Internal reference buffer		ADS7851EVM-PDK
Internal reference	Int $V_{REF}=2.5\text{-V}, \pm 10\text{ppm}/^{\circ}\text{C}$	Internal reference buffer		TI Design: TIPD117
REF5040	REF5040: 4.096-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA350	Wide BW, low output impedance, drift 4 μV/°C	ADS7945EVM-PDK
REF5040	REF5040: 4.0-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA350	Wide BW, low output impedance, drift 4 μV/°C	ADS7946EVM-PDK

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 14 bit

ADC Input Driver			
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS7891</b> 3 MSPS, 1 channel, 0 - 2.5 input range, single ended	THS4031	1) Max throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	<b>Single-ended SAR ADC:</b> Refer to datasheet for performance spec
<b>ADS8674</b> 500 kSPS, 4 channels, single ended	OPA2209	Amplifier only for driving aux channel integrated AFE for input channels	<b>Optimized for dynamic performance:</b> Refer to datasheet for performance spec
<b>ADS8678</b> 500 kSPS, 8 channels, single ended input ranges: $\pm 10.24$ $\pm 5.12$ $\pm 2.56$ 0 - 10.24 0 - 5.12 0 - 2.56 0 - 1.28		1) Max specified throughput 2) AC input specified on datasheet 3) $\pm 15$ -V dual supply	

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF5025	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C		Internal reference buffer	ADS7891EVM
REF5040	<p>1) Internal reference: 4.096V, drift 10ppm/°C max.</p> <p>2) External reference: REF5040 4.096-V, 0.05% init. accuracy, low drift 3ppm/°C</p>		Internal reference buffer	ADS8678EVM-PDK

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 12 bit

	ADC Input Driver		
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS7042</b> 1 MSPS, 1 channel, 0 - 3.6 input range, single ended	OPA314	1) 100 to 500 kSPS throughput 2) 10-kHz AC input 3) 3.3-V single supply	<b>Low power, ultra-small, optimized for 500 kSPS:</b> SNR>70 dB@10 kHz, THD <-75 dB@10kHz, P=1 mW
	OPA835	1) 500 to 1 MSPS throughput 2) 100-kHz AC input 3) -0.7/4.7-V dual supply	<b>Low power, ultra-small, optimized for 1 MSPS:</b> SNR>70 dB@100 kHz, THD<-80 dB@100kHz, P=2.5 mW
	No driver!	1) Throughput <10kSPS 2) 100-Hz AC input	<b>Ultra-Low power, ultra-small,            optimized for &lt;1kSPS:</b> SNR>70 dB@100 Hz, THD<-80 dB@100 kHz, P<1 uW
<b>ADS7043</b> 1 MSPS, 1 channel, 0 - 3.6 input range, pseudo differential	OPA316	1) 100 to 1 MSPS throughput 2) 2-kHz AC input 3) 3.3-V single supply	<b>Low power, ultra-small, optimized for 1 MSPS:</b> SNR>69 dB@2 kHz, THD <-80 dB@2 kHz
	OPA835	1) 500 to 1 MSPS throughput 2) 100-kHz AC input 3) -0.7/4.7-V dual supply	<b>Low power, ultra-small, optimized for 1 MSPS:</b> SNR>69 dB@100 kHz, THD<-80 dB@100 kHz, P=2.5 mW
	No driver!	1) Throughput <10kSPS 2) 100-Hz AC input	<b>Ultra-Low power, ultra-small,            optimized for &lt;1kSPS:</b> SNR>70 dB@100 Hz, THD<-80 dB@100 kHz, P<1 uW
<b>ADS7044</b> 1 MSPS, 1 channel, ±3.6 input range, fully differential	OPA316	1) 100 to 1 MSPS throughput 2) 5-kHz AC input 3) 3.3-V single supply	<b>Low power, ultra-small, optimized for 1 MSPS:</b> SNR>71 dB@5 kHz, THD <-85 dB@5 kHz
	THS4531	1) 500 to 1 MSPS throughput 2) 25-kHz AC input 3) 3.3-V single supply	<b>Low power, ultra-small, high CMRR:</b> SNR=71.6 dB@25 kHz, THD<-85 dB@25 kHz
	No driver!	1) Throughput <10kSPS 2) 100-Hz AC input	<b>Ultra-Low power, ultra-small,            optimized for &lt;1 kSPS</b>
<b>ADS7250</b> 750 kSPS, 2 channels, 0 - 5 input range, pseudo differential	OPA836	1) 750 kSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR=73 dB@10 kHz, THD<-90 dB@10 kHz
	THS4032	1) 750 kSPS throughput 2) 100-kHz AC input 3) ±15-V dual supply	<b>Dual-channel, simultaneous sampling:</b> SNR=73 dB@100 kHz, THD<-90 dB@100 kHz
<b>ADS7251</b> 2 MSPS, 2 channels, ±5 input range, fully differential	OPA836	1) 1.5 MSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>73 dB@100 kHz, THD<-90 dB@100 kHz  <b>Dual-channel, simultaneous sampling            for optical encoders</b>



ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
AVDD supply	N/A	N/A	N/A	TI Design: TIPD168
AVDD supply	N/A	N/A	N/A	TI Design: TIPD168
AVDD supply	N/A	N/A	N/A	TI Design: TIPD168
AVDD supply	N/A	N/A	N/A	ADS7043 datasheet
AVDD supply	N/A	N/A	N/A	ADS7043 datasheet
AVDD supply	N/A	N/A	N/A	TI Design: TIPD168
AVDD supply	N/A	N/A	N/A	ADS7044 datasheet
AVDD supply	N/A	N/A	N/A	ADS7044 datasheet
AVDD supply	N/A	N/A	N/A	TI Design: TIPD168
REF3125	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4ppm/°C	ADS8350EVM-PDK
REF3125	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4ppm/°C	ADS8350 datasheet
Internal reference	Int $V_{REF}=2.5-V, \pm 10\text{ppm}/^{\circ}\text{C}$	Internal reference buffer		ADS7851EVM-PDK TI Design: TIPD117

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Resolution: 12 bit

	ADC Input Driver		
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<b>ADS7253</b> 1 MSPS, 2 channels, 0 - 5 input range, single ended/ pseudo differential	OPA836	1) 1 MSPS throughput 2) 10-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>71.5 dB@10 kHz, THD<-88 dB@10 kHz
	THS4032	1) 1 MSPS throughput 2) 100-kHz AC input 3) $\pm 15$ -V dual supply	<b>Dual-channel, simultaneous sampling:</b> SNR>71.5 dB@100 kHz, THD<-84 dB@100 kHz
<b>ADS7254</b> 1 MSPS, 2 channels, $\pm 5$ input range, fully differential	THS4521	1) 1 MSPS throughput 2) 100-kHz AC input	<b>Dual-channel, simultaneous sampling:</b> SNR>72.5 dB@100 kHz, THD<-88 dB@100 kHz
	THS4531	1) 1 MSPS throughput 2) 20-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling for optical encoders:</b> SNR>73.7 dB@2 kHz, THD <-88.3 dB@2 kHz
	THS4531	1) 1 MSPS throughput 2) 20-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling for HIPERFACE position encoder</b>
<b>ADS8638</b> 1 MSPS, 8 channels, $\pm 10$ input range, single ended bipolar	OPA4140	1) <1 MSPS throughput 2) 2-kHz AC input 3) $\pm 15$ -V dual supply	<b>4/8ch integrated analog inputs for PLC:</b> SNR>72 dB, THD<-76 dB
<b>TLV2553</b> 200 kSPS, 11 channels, 0 - 5 input range, single ended	OPA4140	1) Max throughput 2) 10-kHz sine wave input 3) 5-V single supply	<b>Optimized for dynamic performance:</b> THD = 82 dB, SNR = 72 dB, ENOB = 11.5
<b>ADS7251</b> 2 MSPS, 2 channels, $\pm 5$ input range, fully differential	OPA836	1) 1.5 MSPS throughput 2) 100-kHz AC input 3) 5-V single supply	<b>Dual-channel, simultaneous sampling:</b> SNR>73 dB@100 kHz, THD<-90 dB@100 kHz  <b>Dual-channel, simultaneous sampling for optical encoders</b>

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF3425	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4 $\mu\text{V}/^\circ\text{C}$	ADS7853EVM-PDK
REF3425	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4 $\mu\text{V}/^\circ\text{C}$	ADS7253 data-sheet
REF3425	REF5025: 2.5-V, 0.05% init. accuracy, low drift 3ppm/°C	OPA2350	Dual channel, wide BW, low output impedance, drift 4 $\mu\text{V}/^\circ\text{C}$	ADS7043 data-sheet
Internal reference	Int $V_{\text{REF}}=2.5\text{-V}, \pm 10\text{ppm}/^\circ\text{C}$	Internal reference buffer		TI Design: TIPD117
Internal reference	N/A	N/A		TI Design: TIDA-00202
REF3330	$V_{\text{REF}}=3.3\text{V}, 0.15\%$ init. accuracy, 30ppm/°C	N/A	N/A	TI Design: TIDA-00119
REF3440	(4.096V $\pm$ 0.2% (max) initial accuracy, low 20 ppm/°C (max) drift over -40°C to 125°C)	OPA320	Wideband 20MHz, low Vos drift @ 5 $\mu\text{V}/^\circ\text{C}$ (max), low b'band noise (4.5nV/rt_Hz), RRIO swing on 0-5Vsup	TLV2553EVM-PDK
Internal reference	Int $V_{\text{REF}}=2.5\text{-V}, \pm 10\text{ppm}/^\circ\text{C}$	Internal reference buffer		ADS7851EVM-PDK TI Design: TIPD117

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

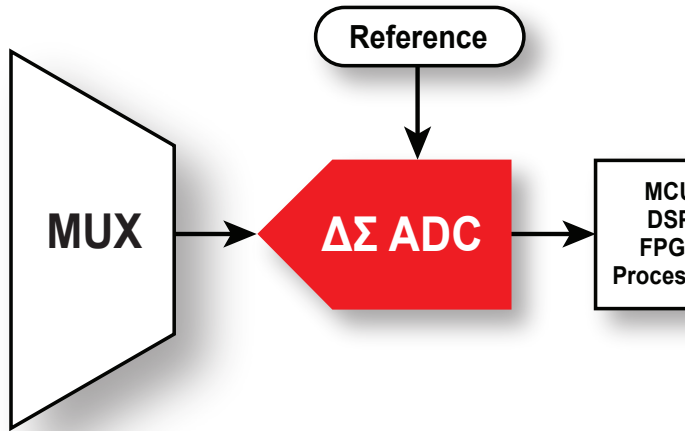
# Resolution: 12 bit

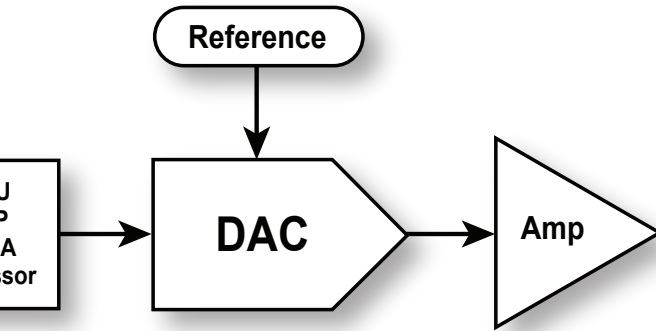
ADC Input Driver			
SAR ADC	Amplifier	Signal chain specifications	Signal chain performance
<p><b>ADS7953</b> 1 MSPS, 16 channels, 0 - 5 input range, single ended</p> <p><b>ADS7952</b> 1 MSPS, 12 channels, 0 - 5 input range, single ended</p> <p><b>ADS7951</b> 1 MSPS, 8 channels, 0 - 5 input range, single ended</p> <p><b>ADS7950</b> 1 MSPS, 4 channels, 0 - 5 input range, single ended</p>	<a href="#">OPA314</a>	<p>ADS7953:</p> <ol style="list-style-type: none"> <li>1) Low power consumption</li> <li>2) &lt;1-kHz AC or DC inputs when all 16 channels scanned sequentially</li> <li>3) 5-V single supply</li> </ol>	<p><b>Low power MUX application:</b> total power &lt; 35 mW</p>
	<a href="#">OPA192</a>	<ol style="list-style-type: none"> <li>1) Buffered multiplexer output (MXO)</li> <li>2) Max throughput</li> <li>3) 5-V single supply</li> </ol>	<p><b>Buffered multiplexer output (MXO):</b> 12-bit settling at 1 MSPS w 1x/2xREF range <math>R(\text{SOURCE}) \leq 10\text{k}\Omega</math></p>
	No Driver!	<ol style="list-style-type: none"> <li>1) 1 MSPS throughput</li> <li>2) <math>R_{\text{source}} \leq 100\Omega</math> for 1xREF Range, 12-bit settling</li> <li>3) <math>R_{\text{source}} \leq 250\Omega</math> for 2xREF Range 12-bit settling</li> </ol>	<p><b>Performance specified on datasheet</b></p>

ADC Reference Driver				
Voltage reference	Reference specifications	Buffer amplifier	Reference driver performance	Reference designs & support
REF3325	2.5V, 0.05% init. accuracy, low drift 3ppm/°C	N/A	N/A	N/A
REF3025				
REF3125				

**NOTE:** Specific voltage references listed in the table were selected to maximize the Signal-to-Noise Ratio (SNR) performance of the signal chain. Other output voltage variants are often available in the same voltage reference product families. Refer to the datasheet for details.

# Delta-Sigma ADC





# Delta-Sigma ADC

Device	Resolution (bits)	Sample rate (kSPS)	No. of channels	Interface	Input Range (V)	Vref
<b>ADS1113</b>	16	0.860	1	I2C	0 to 5.5	Int
<b>ADS1115</b>	16	0.860	4	I2C	0 to 5.5	Int
<b>ADS1115-Q1</b>	16	0.860	4	I2C	0 to 5.5	Int
<b>ADS1118</b>	16	0.860	4	SPI	0 to 5.5	Int
<b>ADS1120</b>	16	2	4	SPI	±2.85, 0 to 5.6	Int, Ext, Supply
<b>ADS1194</b>	16	8	4	SPI	0 to 5.5	Int, Ext
<b>ADS1196</b>	16	8	6	SPI	0 to 5.5	Int, Ext
<b>ADS1198</b>	16	8	8	SPI	0 to 5.5	Int, Ext
<b>ADS1220</b>	24	2	4	SPI	±2.85, 0 to 5.6	Int, Ext, Supply
<b>ADS1247</b>	24	2	4	SPI	±2.125, 0 to 4.25	Int, Ext
<b>ADS1248</b>	24	2	8	SPI	±2.125, 0 to 4.25	Int, Ext
<b>ADS1255</b>	24	30	2	SPI	0 to 5.25	Ext
<b>ADS1262</b>	32	38	10	SPI	±2.5, 0 to 5	Int, Ext
<b>ADS127L01</b>	24	512	1	SPI	0 to 3	Ext
<b>ADS1292</b>	24	8	3	SPI	0 to 5.25	Int, Ext
<b>ADS1293</b>	24	25.6	3	SPI	0 to 0.4	Int
<b>ADS1294</b>	24	32	4	SPI	0 to 5.25	Int, Ext
<b>ADS1294R</b>	24	32	4	SPI	0 to 5.25	Int, Ext
<b>ADS1296</b>	24	32	6	SPI	0 to 5.25	Int, Ext
<b>ADS1296R</b>	24	32	6	SPI	0 to 5.25	Int, Ext
<b>ADS1298</b>	24	32	8	SPI	0 to 5.25	Int, Ext
<b>ADS1298R</b>	24	32	8	SPI	0 to 5.25	Int, Ext
<b>ADS131A04</b>	24	128	4	SPI	-1.5 to 4.5	Int, Ext
<b>ADS131E08</b>	24	64	8	SPI	0 to 4	Int, Ext
<b>ADS131E08S</b>	24	64	8	SPI	0 to 4	Int



# Isolated Amplifier

Device	Input Range (mV)	Isolation Rating (Vpeak)	Min Transient Immunity(kV/uS)	Package	Power (mW)	Operating temperature range Range (°C)
<b>AMC1100</b>	±250	4250	2.5	8SOIC, 8SOP	66	-40 to 105
<b>AMC1200</b>	±250	4250	10	8SOIC, 8SOP	66	-40 to 105
<b>AMC1301</b>	±250	7000	15	8SOIC	45	-40 to 125

# Isolated Modulator

Device	Input Range (mV)	Isolation Rating (Vpeak)	Min Transient Immunity(kV/uS)	Package	Power (mW)	Operating temperature range Range (°C)
<b>AMC1304</b>	±50, ±250	7000	15	16SOIC	59	-40 to 125

# Resolution: 32 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
ADS1262	High resolution, low noise, 32-bit, 38kSPS ADC with PGA and internal reference	32	11	38,400	MSP430 ultra-low-power MCUs
					Signal switches
					Isolation
					Amplifiers and linear
					Step-down (buck)
					Operating temperature range sensors
					Flip-flop/latch/register
					Logic
					Logic
					Logic
					Linear regulator (LDO)
					Step-up (boost)
					Linear regulator (LDO)
					Linear regulator (LDO)

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
MSP430F5529	16-Bit ultra-low-power microcontroller, 128KB flash, 8KB RAM, USB, 12Bit ADC, 2 USCIs, 32Bit HW MPY	High-resolution, low-drift, precision weigh-scale reference design with AC bridge excitation	TIPD188
TS5A21366	0.75-Ohm dual SPST analog switch with 1.8-V compatible input logic		
IS07141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter	Dual channel-to-channel isolated universal analog input module for PLC reference design	TIDA-00550
LM2903	Dual differential comparator		
LM5017	7.5-100V wide vin, 600mA constant on-time synchronous buck regulator		
LMT01	0.5°C accurate 2-pin operating temperature range sensor with a pulse train interface		
SN74AHC594	8-bit shift registers with output registers		
SN74LVC1G125	Single bus buffer gate with 3-state outputs		
SN74LVC1G332	Single 3-input positive-OR gate		
SN74LVC2G86	Dual 2-input exclusive-OR gate		
TLV704	24-V input voltage, 150-mA, ultralow IQ low-dropout regulators		
TPS61093	Low input, 20V/1.1A step-up DC/DC converter with integrated power diode and input/output isolation		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A4101	50V Input, 50mA, single output low-dropout linear regulator		
TPS7A49	Vin 3V to 36V, 150mA, ultra-low noise, high PSRR, low-dropout linear regulator		

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
ADS1194	Multi-channel ADC with integrated ECG front end	16,24	4,6,8	32,000	Op amp
ADS1196					Amplifier
ADS1198					
ADS1294					Logic
ADS1294R					
ADS1296					
ADS1296R					
ADS1298					Op amp
ADS1298R					

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
OPA320	Precision, 20MHz, 0.9pA Ib, RRIO, CMOS op amp	Reference design for software pacemaker detection in an ECG	TIPD197
TLV3401	Single nanopower open drain output comparator		TIPD111
SN74LVC2G00	Dual 2-input positive-NAND gate		
OPA348	1MHz, 45μA, RRIO, single op amp	Hardware pace detection for ECG reference design	

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS1220</b>	Low-power, low-noise, 24-bit ADC for small signal sensors	24	4	2,000	Digital-to-analog converter
					MSP430 ultra-low-power MCUs
					Buffer/driver/transceiver
					Buck/boost, inverting or split-rail
					Linear regulator (LDO)
					Signal switches
					Power MOSFET
					Power MOSFET
					Operating temperature range sensors
					Linear regulator (LDO)
					MSP430 ultra-low-power MCUs
					ESD protection diodes
					Signal switches
					MSP430 ultra-low-power MCUs
					Other interface
					MSP430 ultra-low-power MCUs
					MSP430 ultra-low-power MCUs
					I2C
MSP430 ultra-low-power MCUs					
Other interface					

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
DAC161P997	Single-wire 16-bit DAC for 4-20mA loops	Isolated loop powered thermocouple transmitter reference design	TIDA-00189
MSP430F5172	MSP430F51x2 mixed signal microcontroller		
SN74LVC125A	Quadruple bus buffer gate with 3-state outputs		
TPS60402	60mA charge pump voltage inverter with fixed 50kHz operation		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TS5A3159	1-ohm SPDT analog switch	Wireless thermocouple sensor transmitter DevPack for SensorTag reference design	TIDA-00650
CSD13381F4	12V, n-channel FemtoFET™MOSFET	1g resolution over 15kg range sub-100nVpp noise front-end reference design	TIDA-00765
CSD23381F4	12V, p-channel FemtoFET™MOSFET		
LMT01	0.5°C accurate 2-pin operating temperature range sensor with a pulse train interface		
LP5907	250-mA ultra-low-noise LDO		
MSP430G2553	MSP430G2x53, MSP430G2x13 mixed signal microcontroller		
TPD4E1U06	Quad-channel high-speed ESD protection device		
TS5A21366	0.75-Ohm dual SPST analog switch with 1.8-V compatible input logic		
MSP430FR5738	MSP430FR5738 24 MHz ULP microcontroller with 16 KB FRAM, 1 KB SRAM, 21 IO, 10-bit ADC and comparator	IO-Link adapter reference design for sensors with analog 4-20mA output	TIDA-00457
SN65HVD101	IO-LINK PHY for device nodes		
MSP430FR5949	MSP430FR5949 16 MHz ultra-low-power microcontroller featuring 64 KB FRAM, 2 KB SRAM, 33 IO	Thermocouple AFE using RTD or integrated operating temperature range sensor for cold junction compensation (CJC)	TIDA-00168
REF5020	Low noise, very low drift, precision voltage reference		
TCA9535	Remote 16-Bit I2C and SMBus, low-power I/O expander with interrupt output and config registers		
MSP430FR5738	MSP430FR5738 24 MHz ULP microcontroller with 16 KB FRAM, 1 KB SRAM, 21 IO, 10-bit ADC and comparator	IO-Link sensor transmitter reference design	TIDA-00188
SN65HVD101	IO-Link PHY for device nodes		

# Resolution: 24 bit

## Precision delta-sigma analog-to-digital converters

Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS1220 (cont.)</b>	Low-power, low-noise, 24-bit ADC for small signal sensors	24	4	2,000	Digital-to-analog converter
					MSP430 ultra-low-power MCUs
					Linear regulator (LDO)
					n/a
					Digital-to-analog converter
					MSP430 ultra-low-power MCUs
					Linear regulator (LDO)
					MSP430 ultra-low-power MCUs
					Voltage reference
					I2C
					Isolation
					Isolation
					Step-down (buck)
					Protection, monitoring and hot swap
					Operating temperature range sensors
					I2C
					Operating temperature range sensors
					Linear regulator (LDO)
Linear regulator (LDO)					
Signal switches					



Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
DAC161S997	SPI 16-bit precision DAC for 4-20mA loops	RTD operating temperature range transmitter for 2-wire, 4 to 20-mA current loop systems	TIDA-00095
MSP430G2513	MSP430G2x53, MSP430G2x13 mixed signal microcontroller		
TPS7A49	Vin 3V to 36V, 150mA, ultra-low noise, high PSRR, low-dropout linear regulator		
n/a	n/a	Fast prototyping 24bit sigma-delta ADC with energia ecosystem reference design	TIDA-00647
DAC161S997	SPI 16-bit precision DAC for 4-20mA loops	Small form factor, 2-wire 4 to 20mA current loop RTD operating temperature range transmitter reference design	TIDA-00165
MSP430FR5738	MSP430FR5738 24 MHz ULP microcontroller with 16 KB FRAM, 1 KB SRAM, 21 IO, 10-bit ADC and comparator		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
MSP430FR5949	MSP430FR5949 16 MHz ultra-low-power microcontroller featuring 64 KB FRAM, 2 KB SRAM, 33 IO	Optimized latency, power and memory footprint thermocouple sensing front-end reference design	TIDA-00468
REF5020	Low noise, very low drift, precision voltage reference		
TCA9535	Remote 16-Bit I2C and SMBus, low-power I/O expander with interrupt output and config registers		
ISO1540	2.5 kVrms isolated bidirectional clock, bidirectional I2C isolators	Operating temperature range sensor interface module for programmable logic controllers (PLC)	TIDA-00018
ISO7141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		
LM5017	7.5-100V wide vin, 600mA constant on-time synchronous buck regulator		
LM5069	Positive high voltage hot swap / inrush current controller with power limiting		
LM94022	$\pm$ 1.5°C operating temperature range sensor with multiple gain and class-AB analog output		
TCA6408A	Low-voltage 8-Bit I2C and SMBus I/O expander		
TMP275	$\pm$ 0.5°C operating temperature range sensor with I2C/SMBus interface in industry std LM75 form factor & pinout		
TPS715	50 mA, 24 V, 3.2- $\mu$ A Iq, low-dropout linear regulator in SC70 package		
TPS7A49	Vin 3V to 36V, 150mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TS5A23159	1-ohm 5-V/3.3-V 2-channel SPDT switch		

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS1247</b>	24-bit, 2kSPS, 4-ch ADC for precision sensor measurement	24	4	2,000	Linear regulator (LDO)
<b>ADS1248</b>	24-bit, 2kSPS, 8-ch ADC for precision sensor measurement	24	8	2,000	Magnetic current sensing
					Linear regulator (LDO)
					Power MOSFET
					I2C
					Linear regulator (LDO)
					Signal switches
					Amplifiers and linear
					Instrumentation amplifier
<b>ADS1255</b>	24-bit, 30kSPS very low noise delta-sigma ADC	24	1	30,000	ARM cortex-A8
					Advanced light control
					Advanced light control
					Amplifiers and linear
					Op amp
					Op amp
					Voltage reference
					Power management
					Power management
					Step-down (buck)
					Linear regulator (LDO)

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
TPS7A49	Vin 3V to 36V, 150mA, ultra-low noise, high PSRR, low-dropout linear regulator	0°C – 100°C, hardware-compensated ratiometric 3-wire RTD system reference design	<a href="#">TIPD152</a>
		3-wire RTD Acquisition System Accurately Measures operating temperature range From -200°C - 850°C	<a href="#">TIPD120</a>
DRV425	Fully integrated precision fluxgate magnetic field sensor and readout	Fluxgate based displacement sensor reference design	<a href="#">TIDA-00463</a>
TPS717	150mA, high PSRR, low quiescent current, low noise LDO		
CSD17571Q2	30V n-channel NexFET power MOSFETs, CSD17571Q2	Analog front end (AFE) for sensing operating temperature range in smart grid applications using RTD reference design	<a href="#">TIDA-00110</a>
TCA6408A	Low-voltage 8-bit I2C and SMBus I/O expander		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TS3A5017	14-ohm dual SP4T analog switch 3.3-V/2.5-V analog multiplexer/demultiplexer		
INA159	High-speed, precision gain of 0.2 level translation difference amplifier	Analog input module for industrial outputs and operating temperature range sensors reference design	<a href="#">TIPD164</a>
INA826	Precision, 200- $\mu$ A supply current, 36-V supply instrumentation amplifier		
REF5025	3 $\mu$ Vpp/V noise, 3 ppm/°C drift precision voltage reference		
AM3358	Sitara processor	DLP near-infrared spectrometer for optical analysis of liquids & solids reference design	<a href="#">TIDA-00155</a>
DLP4500NIR	DLP® 0.45 WXGA NIR DMD		
DLPC350	Digital controller for DLP4500 DMD and DLP4500NIR DMD		
INA330	Thermistor signal amplifier for operating temperature range control		
OPA2376	Precision, low noise, low quiescent current op amp		
OPA569	Power op amp, output signal swings within 200mV of rails at 2A output current		
REF5025	3 $\mu$ Vpp/V noise, 3 ppm/°C drift precision voltage reference		
TPS65145	4-CH LCD bias w/ fully int. pos. charge pump, 3.3V LDO contr., 0.96A min. boost ilim & fault detect		
TPS65217	Power management IC (PMIC) w/ 3 DC/DCs, 4 LDOs, linear battery charger & white LED driver		
TPS65251	4.5-V TO 18-V input, 3A/2A/2A output, synchronous triple buck converter		
TPS730	Low-noise, high PSRR, RF 200-mA low-dropout linear regulators		

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS1255 (cont.)</b>	24-bit, 30kSPS very low noise delta-sigma ADC	24	1	30,000	Battery management products
					Wireless connectivity
					Wireless connectivity
					Advanced light control
					Video and data display
					Advanced light control
					Sensor products
					Current sense amplifiers
					Op amp
					Op amp
					Op amp
					Voltage reference
					Logic
					Logic
					Logic
					Logic
					Logic
					Control + automation
					Control + automation
					Operating temperature range sensors
					ESD protection diodes
					Integrated load switches
					Power management
					Buck/boost, inverting or split-rail
Non-isolated module					
Non-isolated module					
Voltage level translation					

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
BQ24250	2A single input I2C/standalone switch-mode li-ion battery charger	DLP ultra-mobile NIR spectrometer for portable chemical analysis with bluetooth connectivity	TIDA-0054
CC2564MODA	Dual-mode Bluetooth® CC2564 module with integrated antenna		
CC2564MODN	Dual-mode Bluetooth® CC2564 module		
DLP2010NIR	DLP® 0.2 WVGA NIR DMD		
DLPA2005	PMIC/LED driver (high current) for DLP2010 DMD and DLP2010NIR DMD		
DLPC150	Digital controller for the DLP2010NIR DMD		
HDC1000	Low power, 3% accuracy digital humidity sensor with integrated operating temperature range sensor		
INA213	26-V, bi-directional, zero-drift, high accuracy, low-/high-side, voltage out current shunt monitor		
OPA2376	Precision, low noise, low quiescent current op amp		
OPA350	High-speed, single-supply, rail-to-rail op amps MicroAmplifier(TM) series		
OPA567	Rail-to-rail I/O, 2A power amplifier		
REF5025	3 $\mu$ Vpp/V noise, 3 ppm/°C drift precision voltage reference		
SN74LV1T04	Single power supply inverter gate logic level shifter		
SN74LV1T08	Single power supply 2-input positive and gate with logic level shifter		
SN74LV1T32	Single power supply 2-input positive or gate with logic level shifter		
SN74LVC1G125	Single bus buffer gate with 3-state outputs		
SN74LVC2G125	Dual bus buffer gate with 3-state outputs		
SN74LVC2G157	Single 2-line to 1-line data selector/multiplexer		
TM4C1290NCZAD	High performance 32-bit ARM® Cortex®-M4F based MCU		
TM4C129DNC PDT	IoT enabled high performance 32-bit ARM® Cortex®-M4F based MCU		
TMP006	Infrared thermopile contactless operating temperature range sensor in WCSP package		
TPD4S012	4-channel USB ESD solution with power clamp		
TPS22904	3.6V, 0.5A, 68m $\Omega$ load switch with quick output discharge		
TPS386596	Quad voltage supervisor with manual reset input		
TPS63036	High efficient single inductor buck-boost converter with 1-A switches		
TPS81256	Integrated power solution, 3W high-efficiency boost converter module in MicroSiP™ package		
TPS82671	600mA fully integrated, low noise step-down converter module in MicroSiP(TM) package		
TXB0106	6-Bit bidirectional voltage-level translator with auto direction sensing and +/-15-kV ESD protect		

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
ADS127L01	24-bit, high-speed, wide-bandwidth analog-to-digital converter	24	1	512,000	Level translation
					Logic
					Signal switches
					ESD protection diode
					Power management
					Step-up (boost)
					Control and automation
					Op amp
					Reference
					ADS1292
Step-up (boost)					
Operating temperature range sensor					
Op amp					
Microcontroller					
Microcontroller					
Voltage reference					
ADS1293	Complete low power integrated AFE for ECG applications	24	3	25,600	Wireless connectivity
					Step-up (boost)

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
<a href="#">TXS0108E</a>	8-Bit bidirectional voltage-level shifter for open-drain and push-pull applications	ADS127L01 24-bit, delta-sigma analog-to-digital converter (ADC) evaluation module	<a href="#">ADS127L01EVM</a>
<a href="#">SN74LVC2G06</a>	Dual inverter buffer/driver with open-drain output		
<a href="#">SN74LVC1G07</a>	Single buffer/driver with open-drain output		
<a href="#">SN74LVC1G06</a>	Single inverter buffer/driver with open-drain output		
<a href="#">SN74AUP1G80</a>	Low-power single positive edge triggered d-type flip-flop		
<a href="#">SN74CBT3244</a>	Octal FET bus switch		
<a href="#">TPD4S012</a>	4-channel USB ESD solution with power clamp		
<a href="#">TLV803M</a>	3-pin voltage supervisor with active-low, open drain reset		
<a href="#">TPS61252</a>	3.5MHz, 1.5A, 92% efficient boost converter with adjustable input current limit		
<a href="#">TM4C1294NCPDT</a>	IoT enabled high performance 32-bit ARM cortex-M4F based MCU		
<a href="#">THS4551</a>	Low noise, precision, 150MHz, fully differential amplifier		
<a href="#">REF6025</a>	High-precision voltage reference with integrated high-bandwidth buffer		
<a href="#">TPS730</a>	Low-noise, high PSRR, RF 200mA low-dropout linear regulators	Multi-parameter bio-signal monitor reference design for personal monitoring	<a href="#">TIDM-BIOSIGN-MONITOR</a>
<a href="#">TPS61220</a>	Low input voltage, 0.7V boost converter with 5.5µA quiescent current		
<a href="#">TMP112</a>	1.4V-capable ±0.5°C operating temperature range sensor with alert function and I2C/SMBus interface		
<a href="#">LMP2231</a>	Single micropower, 1.6V, precision op amp with CMOS inputs		
<a href="#">RF430CL331H</a>	Dynamic NFC interface transponder for large file transfer		
<a href="#">MSP430FRS989</a>	16 MHz ultra-low-power microcontroller featuring 128KB FRAM, 2KB SRAM, 48 I/O, scan IF, AES		
<a href="#">LM4041-N</a>	Precision micropower shunt voltage reference	Wireless heart monitor with bluetooth low energy	<a href="#">TIDA-00096</a>
<a href="#">CC2541</a>	Simplelink bluetooth low energy and proprietary wireless MCU		
<a href="#">TPS61220</a>	Low input voltage, 0.7V boost converter with 5.5µA quiescent current		

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS131A04</b>	24-bit, simultaneous sampling AFE for power monitoring, control and protection	24	4	128,000	Isolator
					LDO
					Charge pump
					Clock buffer
					Reference
					Op amp
					Isolated amplifier
<b>ADS131E08</b>	24-bit, simultaneous sampling AFE for power monitoring, control and protection	24	8	64,000	Isolator
					LDO
					LDO
					LDO
					Microcontroller
					Transistor



Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
SN6501	Low-noise, 350 mA, 410 kHz transformer driver	High accuracy $\pm 0.5\%$ current and isolated voltage measurement reference design	TIDA-00835
TLV704	24V, 150mA ultra-low IQ, low dropout regulator		
TPS717	150mA, high PSRR, low quiescent current, low noise LDO		
TPS723	200mA, negative output low-dropout linear regulator		
TPS60403	60mA charge pump voltage inverter with fixed 250kHz operation		
CDCLVC1102	Low jitter, 1:2 LVCMOS fan-out clock buffer		
LM4040-N	Precision micropower shunt voltage reference		
OPA4180	0.1 $\mu\text{V}/\text{C}$ drift, quad, low noise, rail-to-rail, 36V zero drift op amp		
AMC1301	Precision $\pm 250\text{mV}$ input, 3 $\mu\text{S}$ delay, reinforced isolated amplifier		
SN6501	Transformer driver	Isolated current and voltage measurement using fully differential isolation amplifier reference design	TIDA-00555
TLV704	24V, 150mA ultra-low IQ, low dropout regulator		
TPS7A30	-3V to -36V, -200mA, ultra-low noise, high PSRR, low dropout regulator		
TPS7A6533-Q1	Automotive 300-mA 40-V low-dropout regulator with ultra-low quiescent current		
MSP430FR5869	MSP430FR5869 mixed signal microcontroller		
CSD17571Q2	30V N-channel NexFET power MOSFET output driver		

# Resolution: 24 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS131E08S</b>	24-bit, simultaneous sampling AFE with fast power-up time for power monitoring, control and protection	24	8	64,000	Operating temperature range sensor
					LDO
					Reference
					Power management
					Microcontroller
					Amplifier

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
LMT87	2.7V-capable, 10 $\mu$ A analog output operating temperature range sensor	High resolution, fast startup analog front end for air circuit breaker reference design	TIDA-00661
TPS723	200mA, negative output low-dropout linear regulator		
TPS732	Cap-free, NMOS, 250mA low dropout regulator with reverse current protection		
TPS7A6533-Q1	Automotive 300-mA 40-V low-dropout regulator with ultra-low quiescent current		
LM4041B	Adjustable precision micropower shunt voltage reference 0.2% accuracy		
LM8364	Active low voltage monitor with low quiescent current and 2.5% threshold accuracy		
MSP430FR5969	16 MHz ultra-low-power microcontroller featuring 64KB FRAM, 2KB SRAM, 40 I/O		
LMV614	Low-voltage, low-power, RRIO general purpose amplifier		
LM2903	Dual differential comparator		

# Resolution: 16 bit

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Resolution (bits)	No. of channels	Max sample rate (SPS)	Product type
<b>ADS1113</b>	16-Bit ADC with Integrated MUX, oscillator, and reference	16	1	860	Operating temperature range sensors
					MSP430 ultra-low-power MCUs
					Wireless MCUs
<b>ADS1115</b>	16-Bit ADC with integrated MUX, PGA, comparator, oscillator, and reference	16	4	860	Operating temperature range sensors
					MSP430 ultra-low-power MCUs
					Linear regulator (LDO)
					Operating temperature range sensor
					Microcontroller
					ESD protection diode
<b>ADS1115-Q1</b>	Automotive, 16-bit ADC with Integrated MUX, PGA, comparator, oscillator, and reference	16	4	860,000	Op amp
					Current sense amplifiers
<b>ADS1118</b>	16-Bit ADC with integrated MUX, PGA, operating temperature range sensor, oscillator, and reference	16	4	860	n/a
<b>ADS1120</b>	Low-power, low-noise, 16-bit ADC for small signal sensors	16	4	2,000	Sensor products
					Voltage reference
					Signal switches

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
LMT70	±0.1°C precision analog operating temperature range sensor	Passive NFC operating temperature range patch reference design	TIDA-00721
MSP430G2403	MSP430G2x33, MSP430G2x03 mixed signal microcontroller		
RF430CL330H	Dynamic dual interface NFC transponder		
LMT70	±0.1°C precision analog operating temperature range sensor	Human skin operating temperature range sensing for wearable applications reference design	TIDA-00824
MSP430G2553	MSP430G2x53, MSP430G2x13 mixed signal microcontroller		
TPD1E10B06	Single-channel ESD in 0402 package With 10pF capacitance and 6V breakdown		
LMT70	±0.1°C precision analog operating temperature range sensor		
MSP430G2553	Mixed signal microcontroller		
TPD1E10B06	Single-channel ESD in 0402 package with 10pF capacitance and 6V breakdown		
TLC2272A-Q1	Automotive advanced linCMOS rail-to-rail op amp	Voltage and current measurement reference design for automotive on-board charger system	TIDA-00456
INA138-Q1	Automotive grade, 36V, high-side, current output current shunt monitor		
n/a	n/a	Simple thermocouple measurement solution reference design, <1°C accurate	TIPD109
LMP91200	Integrated AFE for low-power pH sensing applications	Wireless pH sensor transmitter (DevPack for SensorTag) reference design	TIDA-00561
REF3120	2.048 4ppm/degrees C, 100uA SOT23-6 series (bandgap) voltage reference		
TS5A3159	1-Ohm SPDT analog switch		

# Isolated Amplifier

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Gain Error (%)	No. of channels	Output type	Product type
AMC1100	4.25kV peak isolated amplifier for current shunt measurements	±0.05	1	Voltage	Transformer driver
					LDO
					LDO
					LDO
					Microcontroller
					Transistor
					Analog-to-digital converter
AMC1200	4.25kV peak isolated amplifier for current shunt measurements	±0.05	1	Voltage	DC/DC switching regulator
					Transformer driver
					Op amp
					Voltage reference
					Voltage reference
					Transformer driver
					Isolated gate driver
					MOSFET gate driver
					N-channel MOSFET
					DC/DC switching regulator
					LDO
					Non-inverting driver
					Transformer driver

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
SN6501	Low-noise 350 mA, 410 kHz transformer driver	Isolated current and voltage measurement using fully differential isolation amplifier reference design	TIDA-00555
TLV704	24-V input voltage, 150-mA, ultralow IQ low-dropout regulators		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A6533-Q1	Automotive 300-mA 40-V low-dropout regulator with ultra-low quiescent current		
MSP430FR5869	MSP430FR5869 mixed signal microcontroller		
CSD17571Q2	30V N-channel NexFET power MOSFET output driver		
ADS131E08	Analog Front-End for power monitoring, control and protection		
LM5017MR	7.5-100V wide vin, 600mA constant on-time synchronous buck regulator	Isolated bias supplies and isolated amplifier combo for line voltage or current measurement	PMP9480
SN6501	Low-noise 350 mA, 410 kHz transformer driver	Shunt-based 200A peak current measurement reference design using isolation amplifier	TIDA-00445
OPA2376,OPA376	Precision, low noise, low quiescent current op amp		
REF2033	Low-drift, low-power, dual-output VREF and VREF/2 voltage reference		
LM4040	Precision micropower shunt voltage reference		
SN6501	Low-voise 350 mA, 410 kHz transformer driver	Isolated IGBT gate driver evaluation platform for 3-phase inverter system reference design	TIDA-00195
IS05852S	5.7 kVrms split O/P, reinforced isolated IGBT gate driver		
UCC27211	120V boot, 4A peak, high frequency high-side low-side driver		
CSD88537ND	60V dual n-channel NexFET power MOSFET		
TPS54286	4.5V to 28V input, dual 2A output, 600kHz step-down converter with internal compensation		
LP38691	500mA low dropout CMOS linear regulators stable with ceramic output capacitors		
SN74ALVC125	Quadruple bus buffer gates with 3-state outputs		
SN6501	Low-noise 350 mA, 410 kHz transformer driver	Isolated current sensing reference design solution, 5A, 2kV	TIPD121

# Isolated Amplifier

## Precision delta-sigma analog-to-digital converters

Product number	Product description	Gain Error (%)	No. of channels	Output type	Product type
<b>AMC1301</b>	Precision +/-250-mV input, 3-us delay, reinforced isolated amplifier	±0.05	1	Voltage	Isolated gate driver
					Op amp
					Comparator
					LDO
					LDO
					Voltage reference
					Voltage reference
					Logic gate
					Microcontroller



Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
UCC21520	4A/6A, 5.7 kVrms isolated dual channel gate driver	Reference design for reinforced isolation 3-phase inverter with current, voltage and temp protection	TIDA-00366
OPA320	Precision, 20MHz, 0.9pA Ib, RRIO, CMOS op amp		
TLC372	Dual general purpose LinCMOS™ differential comparator		
TLCV1117-33	Single output LDO, 800mA, fixed(3.3), internal current limit, thermal overload protection		
TLV704	24-V input voltage, 150-mA, ultralow IQ low-dropout regulators		
REF2033	Low-drift, low-power, dual-output VREF and VREF/2 voltage reference		
TL431B	Adjustable precision shunt regulator		
SN74LVC1G10	Single 3-input positive-NAND gate		
TMS320F28027	Piccolo microcontroller		

# Isolated Modulator

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Gain Error (%)	No. of channels	Output type	Product type
<b>AMC1304</b>	Reinforced isolated modulator with LDO regulator	±0.02	1	Bit stream	Transformer driver
					LDO
					LDO
					Op amp
					Clock generator
					DC/DC switching regulator
					LDO
					DC/DC switching regulator
					Voltage reference
					Digital-to-analog converter
					Microcontroller
					Transformer driver
					LDO
					Microcontroller

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
SN6501	Low-noise 350 mA, 410 kHz transformer driver	Shunt-based AC/DC current and voltage sensing for smart grid applications with reinforced isolation	TIPD165
TLV704	24-V input voltage, 150-mA, ultralow IQ low-dropout regulators		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
OPA211	1.1nV/rtHz noise, low-power, precision op amp		
CDCE906	Programmable 3-PLL clock synthesizer / multiplier / divider		
TPS55340	Integrated, 5-A 40-V wide input range boost/SEPIC/flyback DC-DC regulator		
LP38798	800-mA, high PSRR ultra-low-noise LDO for RF and analog circuits		
TPS54232	3.5V to 28V input, 2A, 1MHz step-down converter with eco-mode		
REF3012	1.25V 50ppm/degrees C, 50uA in SOT23-3 series (bandgap) voltage reference		
DAC8564	16-bit, quad channel, ultra-low glitch, voltage output DAC with 2.5V, 2ppm/°C internal reference		
TMS320F28377D	Dual-core delfino microcontroller	Isolated, shunt-based current sensing reference design	TIDA-00171
SN6501	Low-noise 350 mA, 410 kHz transformer driver		
TPS795	Ultralow-noise, high-PSRR, fast, RF, 500-mA low-dropout linear regulators		
TMS320F28377D	Dual-core delfino microcontroller		

# Isolated Modulator

Precision delta-sigma analog-to-digital converters					
Product number	Product description	Gain Error (%)	No. of channels	Output type	Product type
<b>AMC1304 (cont.)</b>	Reinforced isolated modulator with LDO regulator	±0.02	1	Bit stream	Transformer driver
					LDO
					LDO
					Op amp
					Instrumentation amplifier
					Clock generator
					DC/DC switching regulator
					LDO
					DC/DC switching regulator
					Voltage reference
					Digital-to-analog converter
					Microcontroller
					Non-inverting driver
					Transformer driver
					LDO
					LDO
					LDO
					Op amp
Clock buffer					
Microcontroller					

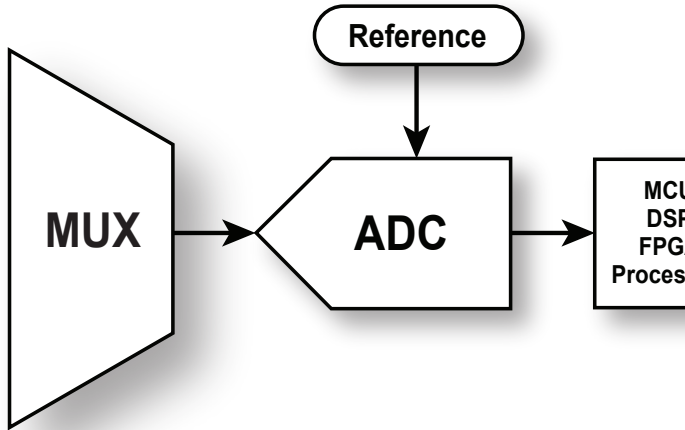
Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
SN6501	Low-noise 350 mA, 410 kHz transformer driver	Isolated current shunt and voltage measurement reference design for motor drives	TIDA-00171
TLV704	24-V input voltage, 150-mA, ultralow IQ low-dropout regulators		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
OPA211	1.1nV/rtHz noise, low-power, precision op amp		
INA826	Precision, 200- $\mu$ A supply current, 36-V supply instrumentation amplifier		
CDCE906	Programmable 3-PLL clock synthesizer / multiplier / divider		
TPS55340	Integrated, 5-A 40-V wide input range boost/SEPIC/flyback DC-DC regulator		
LP38798	800-mA, high PSRR ultra-low-noise LDO for RF and analog circuits		
TPS54232	3.5V to 28V input, 2A, 1MHz step-down converter with eco-mode		
REF3012	1.25V 50ppm/degrees C, 50uA in SOT23-3 series (bandgap) voltage reference		
DAC8564	16-bit, quad channel, ultra-low glitch, voltage output DAC with 2.5V, 2ppm/ $^{\circ}$ C internal reference		
TMS320F28377D	Dual-core delfino microcontroller		
SN74ALVC125	Quadruple bus buffer gates with 3-state outputs		
SN6501	Low-noise 350 mA, 410 kHz transformer driver	Extended current and voltage measurement using shunts for protection relays reference design	TIDA-00738
TLV704	24-V input voltage, 150-mA, ultralow IQ low-dropout regulators		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS709	150-mA, 30-V, ultra-low IQ, wide input low-dropout regulator with reverse current protection		
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp		
CDCLVC1104	Low jitter, 1:4 LVCMOS fan-out clock buffer		
AM4379	Sitara processor		

# Isolated Modulator

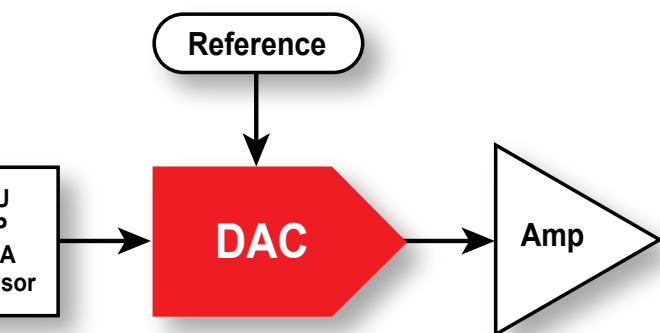
## Precision delta-sigma analog-to-digital converters

Product number	Product description	Gain Error (%)	No. of channels	Output type	Product type
<b>AMC1304 (cont.)</b>	Reinforced isolated modulator with LDO regulator	±0.02	1	Bit stream	Transformer driver
					RS232 transceiver
					Digital isolator
					Digital isolator
					LDO
					Microcontroller

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
<a href="#">SN6501</a>	Low-noise 350 mA, 410 kHz transformer driver	Multi-phase energy measurement with isolated shunt sensors reference design	<a href="#">TIDA-00601</a>
<a href="#">TRS3232</a>	3V to 5.5V multichannel RS-232 line driver/receiver with +/-15kV ESD protection		
<a href="#">ISO7321</a>	Low power, dual-channel 1/1 digital isolator		
<a href="#">ISO7320</a>	Low power, dual-channel 2/0 digital isolator		
<a href="#">TPS793</a>	High PSRR, low noise, 200mA single output LDO		
<a href="#">MSP430F67641</a>	Low cost 3-phase metering SOC		







# DAC

Device	Resolution (bits)	DAC Channels	Interface	INL (Max, $\pm$ LSB)	Output Type	Output Range (Min/Max, mV)
<b>DAC7311</b>	12	1	SPI	3	Buffered Voltage, Current	0 to 24
<b>DAC7750</b>	12	1	SPI	1	Buffered Voltage	0 to 5.5
<b>DAC7760</b>	12	1	SPI	2	Current	0 to 24
<b>DAC7811</b>	16	2	SPI	12	Buffered Voltage	0 to 5.5
<b>DAC8560</b>	16	1	SPI	8	Buffered Voltage	0 to 5.5
<b>DAC8563</b>	12	1	SPI	1	Current	0 to 1
<b>DAC8750</b>	16	1	SPI	52	Buffered Voltage, Current	0 to 24
<b>DAC8760</b>	16	1	SPI	26	Current	0 to 24
<b>DAC8811</b>	16	1	SPI	1	Current	0 to 2
<b>DAC8812</b>	18	1	SPI	2	Buffered Voltage	0 to 5.5
<b>DAC8871</b>	16	2	SPI	1	Current	0 to 2
<b>DAC9881</b>	16	1	SPI	1	Unbuffered Voltage	$\pm$ 18

Operating temperature range Range (°C)	Settling Time (μs)
-40 to 125	10
-40 to 125	6
-40 to 125	10
-40 to 125	7
-40 to 105	8
-40 to 125	0.2
-40 to 125	10
-40 to 125	10
-40 to 85	0.5
-40 to 105	5
-40 to 85	0.5
-40 to 105	1

# Resolution: 18 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
DAC9881	18-bit, 1-channel, low noise, buffered voltage output DAC	18	1	Buffered voltage	Op amp
					Signal switch

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
<a href="#">OPA2192</a>	36-V, precision, RRIO, low offset voltage, low input bias current op amp with e-trim	DAC sample and hold glitch reduction reference design	<a href="#">TIPD142</a>
<a href="#">TS12A4515</a>	Low-voltage, low on-state resistance SPST CMOS analog switches		

# Resolution: 16 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
<b>DAC8563</b>	16-bit, 2-channel, low power, buffered voltage output DAC with 2.5V, 4ppm/°C reference	16	2	Buffered voltage	Op amp
					Isolator
					Interface
					LDO
					LDO
					Microcontroller
					Op amp
					Isolator
<b>DAC8812</b>	16-bit, 2-channel ultra high accuracy, multiplying DAC	16	2	Current	Op amp
<b>DAC8811</b>	16-bit, 1-channel ultra high accuracy, multiplying DAC	16	1	Current	Op amp
<b>DAC8871</b>	16-bit 1-channel, serial Interface, +/-18V (high voltage bipolar) unbuffered voltage output DAC	16	1	Unbuffered voltage	Op amp
					Op amp
					Op amp
					Voltage reference
					Current sense amplifier
					Op amp

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
XTR300	Industrial analog current/voltage output driver	Three phase AC voltage and current transducer with isolated DC analog outputs reference design	TIDA-00454
IS07341FC	4kVpk low power quad channel, 25Mbps digital isolator		
TPL7407L	40V, 7-channel NMOS array, low side driver		
TPS7A4201	28V input, 50mA, single output low-dropout linear regulator		
TPS7A6533-Q1	Automotive 300-mA 40-V low-dropout regulator with ultra-low quiescent current		
MSP430F67661A	MSP430F67661A mixed signal microcontroller		
XTR300	Industrial analog current/voltage output driver	Two-channel source/sink combined voltage & current output, isolated, EMC/EMI tested reference design	TIPD155
IS07641FM	6kVpk low power quad channel, 150Mbps digital isolator		
OPA277	High precision op amps	Digitally tunable MDAC based state variable filter reference design	TIPD160
OPA2277	Ultra low offset, high precision op amp	$\pm 10V$ four-quadrant multiplying DAC reference design	TIPD137
OPA140	11MHz, single supply, low noise, precision, rail-to-rail output, JFET amplifier	SPI programmable 16-bit, 36V, 1A power supply with integrated current shunt	TIPD138
OPA2277	High precision op amps		
OPA549	High-voltage, high-current op amp, excellent output swing		
REF102	Precision voltage reference		
INA146	High-voltage, programmable gain difference amplifier		
LF411	Precision JFET-input op amp		

# Resolution: 16 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
<b>DAC8560</b>	16-bit, 1-channel, buffered voltage output DAC with 2.5V, 2ppm/°C internal reference	16	1	Buffered voltage	Op amp
					Op amp
					Op amp
					Voltage reference
					Signal switch
					Battery charger
<b>DAC8760</b>	16-bit, 1-channel, programmable current/voltage output DAC for 4-20mA current loop applications	16	1	Buffered voltage and current	Op amp
					Isolator
					Op amp
					Isolator
					Switching regulator
					Hot-swap controller
					LDO
					LDO
					LDO
					Op amp
					Isolator



Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
OPA180	0.1- $\mu\text{V}/^\circ\text{C}$ drift, low-noise, rail-to-rail output, 36-V, zero-drift op amp	Bipolar +/-10V output from a unipolar DAC for industrial voltage drivers	TIPD125
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp		
OPA334	0.05 $\mu\text{V}/\text{C}$ max, single-supply CMOS op amp	Charger booster pack reference design	TIDA-00587
REF3425	2.5V 4ppm/degrees C, 100 $\mu\text{A}$ SOT23-6 series (bandgap) voltage reference		
TS3A44159	0.45- $\Omega$ quad SPDT analog switch quad-channel 2:1 multiplexer/demultiplexer with two controls		
BQ24250	2A single input I2C/standalone switch-mode Li-Ion battery charger		
OPA192	High voltage, rail-to-rail input/output, op amps, E-trim™ series	Combined voltage and current output terminal for analog outputs (AO) in industrial applications	TIPD119
IS07641FC	4kVpk low power quad channel, 25Mbps digital isolator		
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp	16-bit analog output module reference design for programmable logic controllers (PLC)	TIDA-00118
IS07141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		
LM5017	7.5-100V wide Vin, 600mA constant on-time synchronous buck regulator		
LM5069	Positive high voltage hot-swap / inrush current controller with power limiting		
TPS7A16	60V, 5- $\mu\text{A}$ Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A47	36-V, 1-A, 4.17- $\mu\text{VRMS}$ , RF LDO voltage regulator		
OPA2171	36V, low power, RRO, general purpose op amp in micropackages	Adaptive power supply for PLC analog output module with output channel protection	TIDA-00231
IS07141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		

# Resolution: 16 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
<b>DAC8760 (cont.)</b>	16-bit, 1-channel, programmable current/voltage output DAC for 4-20mA current loop applications	16	1	Buffered voltage and current	Switching regulator
					Hot-swap controller
					Isolator
					Switching regulator
					Signal switch
					Op amp
					SAR ADC
					LDO
					Interface
					I/O expander
					Op amp
					SAR ADC
					Isolator
					Isolator
					Switching regulator
					Hot-swap controller
					Logic
					LDO
					LDO
					LDO
Voltage Reference					
Isolator					

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
LM5017	7.5-100V wide Vin, 600mA constant on-time synchronous buck regulator	Adaptive power supply for PLC analog output module with output channel protection	TIDA-00231
LM5069	Positive high voltage hot-swap / inrush current controller with power limiting		
ISO7420	Low power dual channel isolators		
TPS61170	1.2A switch, high voltage boost converter in 2x2mm QFN package		
TS5A3159	1-Ohm SPDT analog switch		
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp	Analog input, output and relay drive output module for smart grid IEDs reference design	TIDA-00310
ADS8684	SAR ADC with 16 bits, 4 channels, 500 kSPS, and bipolar inputs off +5V supply		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TPL7407L	40V, 7-channel NMOS array, low side driver		
TCA6408A	Low-voltage 8-bit I2C and SMBus I/O expander		
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp	16-bit analog mixed input and output module for programmable logic controller (PLC) reference design	TIDA-00170
ADS8688	SAR ADC with 16 bits, 8 channels, 500 kSPS, and bipolar inputs off +5V supply		
ISO7141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		
ISO7421	Dual channel, 1/1, 1Mbps digital isolator		
LM5017	7.5-100V wide Vin, 600mA constant on-time synchronous buck regulator		
LM5069	Positive high voltage hot-swap / inrush current controller with power limiting		
SN74HC595	8-bit shift registers with 3-state output registers		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A47	36-V, 1-A, 4.17- $\mu$ VRMS, RF LDO voltage regulator		
REF3425	2.5V low-drift, low-power, small-footprint series voltage reference	Single-channel industrial voltage & current output driver, isolated, EMC/EMI tested reference design	TIPD153
ISO7341FC	4kVpk low power quad channel, 25Mbps digital isolator		

# Resolution: 16 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
<b>DAC8750</b>	16-bit, 1-channel, programmable current output DAC for 4-20mA current loop applications	16	1	Buffered current	Op amp
					Isolator
					Switching regulator
					Hot-swap controller
					LDO
					LDO
					LDO
					Isolator

Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp	16-bit analog output module reference design for programmable logic controllers (PLC)	TIDA-00118
IS07141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		
LM5017	7.5-100V wide Vin, 600mA constant on-time synchronous buck regulator		
LM5069	Positive high voltage hot-swap / inrush current controller with power limiting		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A47	36-V, 1-A, 4.17- $\mu$ VRMS, RF LDO voltage regulator		
IS07341FC	4kVpk low power quad channel, 25Mbps digital isolator	Single-channel industrial voltage & current output driver, isolated, EMC/EMI tested reference design	TIPD153

# Resolution: 12 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
<b>DAC7760</b>	12-bit, 1-channel, programmable current/voltage output DAC for 4-20mA current loop applications	12	1	Buffered voltage and current	Op amp
					Isolator
					Op amp
					Isolator
					Switching regulator
					Hot-swap controller
					LDO
					LDO
					LDO
					Isolator
					Isolator

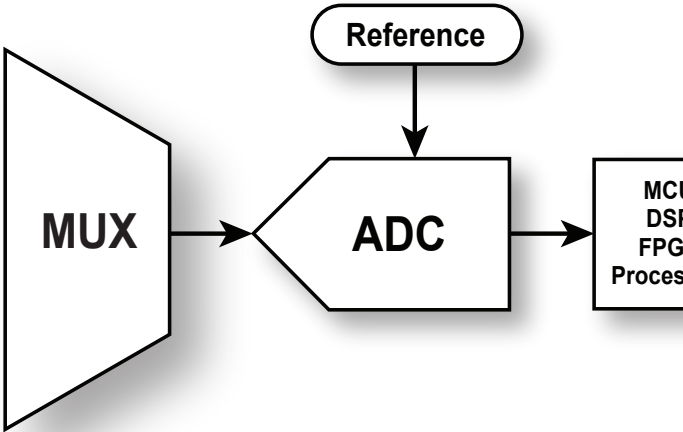
Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
OPA192	High voltage, rail-to-rail input/output, op amps, E-trim™ series	Combined voltage and current output terminal for analog outputs (AO) in industrial applications	TIPD119
IS07641FC	4kVpk low power quad channel, 25Mbps digital isolator		
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp	16-bit analog output module reference design for programmable logic controllers (PLC)	TIDA-00118
IS07141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		
LM5017	7.5-100V wide Vin, 600mA constant on-time synchronous buck regulator		
LM5069	Positive high voltage hot-swap / inrush current controller with power limiting		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A47	36-V, 1-A, 4.17- $\mu$ VRMS, RF LDO voltage regulator		
IS07341FC	4kVpk low power quad channel, 25Mbps digital isolator	Single-channel industrial voltage & current output driver, isolated, EMC/EMI tested reference design	TIPD153
IS07341FC	4kVpk low power quad channel, 25Mbps digital isolator		

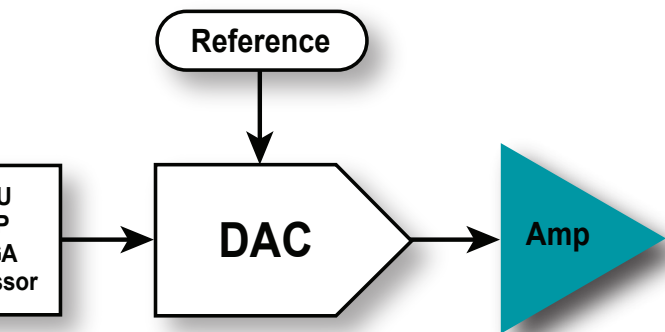
# Resolution: 12 bit

Precision digital-to-analog converters					
Product number	Product description	Resolution (bits)	No. of channels	Output type	Product type
<b>DAC7750</b>	12-bit, 1-channel, programmable current output DAC for 4-20mA current loop applications	12	1	Buffered current	Op amp
					Isolator
					Switching regulator
					Hot-swap controller
					LDO
					LDO
					LDO
<b>DAC7311</b>	12-bit, 1-channel, ultra low power, 2.0V to 5.5V, buffered voltage output DAC	12	1	Buffered voltage	Op amp
					Shunt reference
<b>DAC7811</b>	12-bit, 1-channel, multiplying DAC	12	1	Current	Op amp
					Voltage reference
					Op amp
					Voltage reference



Companion products		Reference design	
Product number	Product description	System highlight (performance)	Document reference
OPA188	Precision, low noise, rail-to-rail output, 36V zero-drift op amp	16-bit analog output module reference design for programmable logic controllers (PLC)	TIDA-00118
IS07141CC	4242-VPK small-footprint and low-power 3/1 quad channel digital isolator with noise filter		
LM5017	7.5-100V wide Vin, 600mA constant on-time synchronous buck regulator		
LM5069	Positive high voltage hot-swap / inrush current controller with power limiting		
TPS7A16	60V, 5- $\mu$ A Iq, 100-mA, low-dropout voltage regulator with enable and power good		
TPS7A30	Vin -3V to -36V, -200mA, ultra-low noise, high PSRR, low-dropout linear regulator		
TPS7A47	36-V, 1-A, 4.17- $\mu$ VRMS, RF LDO voltage regulator		
OPA317	Low offset, rail-to-rail I/O op amp	Low cost loop-powered 4-20mA transmitter EMC/EMI tested reference design	TIPD158
TL431B	Low cost, programmable shunt reference		
OPA192	High voltage, rail-to-rail input/output, op amps, E-trim™ series	Voltage-mode multiplying DAC reference design	TIPD159
REF5025	Low noise, very low drift, precision voltage reference		
OPA376	Precision, low noise, low Iq op amp	Single-supply, unipolar, multiplying DAC reference design	TIPD157
REF5050	Low noise, very low drift, precision voltage reference		





Part number	No. of channels	Power supply range (V)	GBW (typ) (MHz)	Vn at 1kHz (typ) (nV/rtHz)	Slew rate (typ) (V/us)	Rail-to-rail
<b>OPA625</b>	2	2.7-5.5	80	3.2	115	In to V-, out
<b>OPA350</b>	1	2.7-5.5	38	7	22	In, out
<b>OPA320</b>	1	1.8-5.5	20	8.5	10	In, out
<b>THS4521</b>	1	2.5-5.5	95	4.6	490	In to V-, out
<b>OPA333</b>	1	1.8-5.5	0.35	55	0.16	In, out
<b>OPA363</b>	1	1.8-5.5	7	17	5	In, out
<b>OPA313</b>	1	1.8-5.5	1	25	0.5	In, out
<b>THS4031</b>	1	10-30	200	1.6	100	No
<b>THS4131</b>	1	4-33	170	1.3	52	No
<b>THS4032</b>	2	10-30	200	1.6	100	No
<b>OPA836</b>	1	2.5-5.5	205	4.6	560	In to V-, out
<b>OPA192</b>	1	4.5-36	10	5.5	20	In, out
<b>OPA140</b>	1	4.5-36	11	5.1	20	In to V-, out
<b>OPA364</b>	1	1.8-5.5	7	17	5	In, out
<b>THS4531A</b>	1	2.5-5.5	27	10	220	In to V-, out
<b>OPA211</b>	1	4.5-36	45	1.1	27	Out
<b>OPA725</b>	1	4-12	20	10	30	In to V-, out
<b>OPA726</b>	1	4-12	20	10	30	In to V-, out
<b>OPA132</b>	1	5-36	8	8	20	In, out
<b>OPA2209</b>	2	4.5-36	18	2.2	6.4	Out
<b>OPA314</b>	1	1.8-5.5	3	14	1.5	In, out

Vos (max) (mV)	Iq/ch (typ) (mA)	Operating temperature range range (C)	Package group
0.1	2	-40 to 125	SOT-23
0.5	5.2	-40 to 85	PDIP, SOIC, VSSOP
0.15	1.5	-40 to 125	SOT-23
2	1.14	-40 to 85	SOIC, VSSOP
0.01	0.017	-40 to 125	SOIC, SOT-23, SC70
2.5	0.65	-40 to 125	SOIC, SOT-23
2.5	0.05	-40 to 125	SC70, SOT-23
2	8.5	-40 to 85, 0 to 70	MSOP-PowerPAD, SOIC
2	12.3	-40 to 85, 0 to 70	MSOP-PowerPAD, SOIC, VSSOP
2	8.5	-40 to 85, 0 to 70	MSOP-PowerPAD, SOIC
0.4	1	-40 to 125	QFN, SOT-23
0.025	1	-40 to 125	SOIC, SOT-23, VSSOP
0.12	1.8	-40 to 125	SOIC, SOT-23, VSSOP
2.5	0.65	-40 to 125	SOIC, SOT-23
0.4	0.25	-40 to 125	QFN, SOIC, VSSOP
0.05	3.6	-40 to 125	SOIC, SON, VSSOP
3	4.3	-40 to 125	SOIC, SOT-23
3	4.3	-40 to 125	SOIC, VSSOP
0.5	4	-40 to 125	SOIC
0.15	2.2	-40 to 125	VSSOP, SOIC
2.5	0.15	-40 to 125	SC70, SOT-23

Part number	No. of channels	Power supply range (V)	GBW (typ) (MHz)	Vn at 1kHz (typ) (nV/rtHz)	Slew rate (typ) (V/us)	Rail-to-rail
<b>OPA835</b>	1	2.5-5.5	57	9.3	160	In to V-, Out
<b>OPA316</b>	1	1.8-5.5	10	11	6	In, Out
<b>OPA4140</b>	4	4.5-36	11	5.1	20	In to V-, Out
<b>OPA378</b>	1	2.2-5.5	0.9	20	0.4	In, Out
<b>THS4281</b>	1	2.7-16.5	95	12.5	35	In, Out
<b>OPA188</b>	1	4-36	2	8.8	0.8	In to V-, Out
<b>OPA2350</b>	2	2.7-5.5	38	7	22	In, Out
<b>OPA2365</b>	2	2.2-5.5	50	4.5	25	In, Out
<b>OPA376</b>	1	2.2-5.5	5.5	7.5	2	In, Out
<b>OPA2192</b>	2	4.5-36	10	5.5	20	In, Out
<b>OPA277</b>	1	4-36	1	8	0.8	In, Out
<b>OPA2277</b>	2	4-36	1	8	0.8	In, Out
<b>OPA549</b>	1	8-60	0.9	70	9	In to V-
<b>LF411</b>	1	7-36	3	18	15	No
<b>OPA180</b>	1	4-36	2	10	0.8	In to V-, Out
<b>OPA334</b>	1	2.7-5.5	2	60	1.6	In to V-, Out
<b>OPA2171</b>	2	2.7-36	3	14	1.5	In to V-, Out
<b>OPA317</b>	1	1.8-5.5	0.3	55	0.15	In, Out
<b>OPA2376</b>	2	2.2-5.5	5.5	7.5	2	In, Out
<b>OPA350</b>	1	2.7-5.5	38	7	22	In, Out

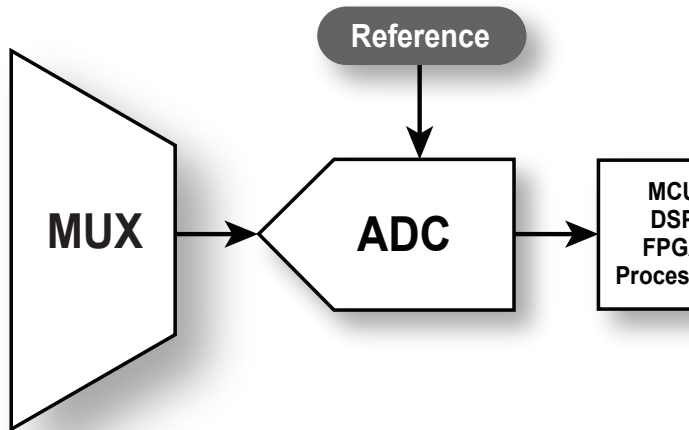
Vos (max) (mV)	Iq/ch (typ) (mA)	Operating temperature range range (C)	Package group
0.5	0.25	-40 to 125	QFN, SOT-23
2.5	0.4	-40 to 125	SC70, SOT-23
0.12	1.8	-40 to 125	SOIC, TSSOP
0.05	0.125	-40 to 125	SC70, SOT-23
2.5	0.8	-40 to 85	SOIC, SOT-23, VSSOP
0.025	0.425	-40 to 125	SOIC, SOT-23, VSSOP
0.5	5.2	-40 to 85	SOIC, VSSOP
0.2	4.6	-40 to 125	SOIC
0.025	0.76	-40 to 125	SC70, SOIC, SOT-23
0.025	1	-40 to 125	SOIC, VSSOP
0.02	0.79	-40 to 85	PDIP, SOIC, VSON
0.025	0.79	-40 to 85	PDIP, SOIC, VSON
5	26	-40 to 85	Power Package, TO-220
1	2	0 to 70	PDIP, SOIC
0.075	0.45	-40 to 105	SOIC, SOT-23, VSSOP
0.005	0.285	-40 to 125	SOT-23
1.8	0.475	-40 to 125	SOIC, VSSOP
0.09	0.021	-40 to 125	SC70, SOIC, SOT-23
0.025	0.76	-40 to 125	DSBGA, SOIC, VSSOP
0.5	5.2	-40 to 85	PDIP, SOIC, VSSOP

Part number	No. of channels	Power supply range (V)	GBW (typ) (MHz)	Vn at 1kHz (typ) (nV/rtHz)	Slew rate (typ) (V/us)	Rail-to-rail
<b>OPA567</b>	1	2.7-5.5	1.2	12	1.2	In, Out
<b>OPA569</b>	1	2.7-5.5	1.2	12	1.2	In to V-, Out
<b>OPA4180</b>	4	4-36	2	10	0.8	In to V-, Out
<b>THS4551</b>	1	2.7-5.4	135	3.3	220	In to V-, Out
<b>TLC2272A-Q1</b>	2	4.4-16	2.18	9	3.6	In to V-, Out
<b>LMP2231</b>	1	1.6-5.5	0.13	60	0.048	In to V-, Out
<b>OPA348</b>	1	1.6-5.5	1	35	0.5	In, Out

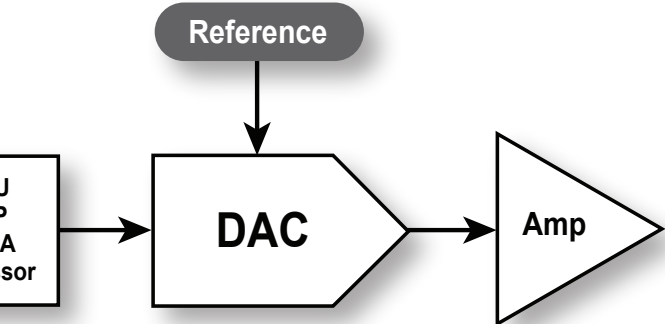


Vos (max) (mV)	Iq/ch (typ) (mA)	Operating temperature range range (°C)	Package group
2	3.4	-55 to 125	VQFN
2	9	-55 to 125	SO PowerPAD
0.075	0.45	-40 to 105	SOIC, TSSOP
0.175	1.35	-40 to 125	QFN, VSSOP
0.95	1.1	-40 to 125	SOIC, TSSOP
0.15	0.01	-40 to 125	SOIC, SOT-23
5	0.045	-40 to 125	SC70, SOIC, SOT-23

# Precision voltage references



# Precision voltage references



# Precision voltage references

Part number	Voltage output V	Temp drift (max) ppm/°C	1/f Noise 0.1 to 10 Hz (typ) $\mu\text{Vp-p}$	Initial accuracy (max) %	Quiescent current (typ) $\mu\text{A}$	Output current mA
REF1925	2.5, 1.25	25 <sup>(4)</sup>	30	0.1	360	20
REF1930	3, 1.5		36			
REF1933	3.3, 1.65		39.6			
REF1941	4.096, 2.048		49.2			
REF2025	2.5, 1.25	8 <sup>(4)</sup>	30	0.05	360	20
REF2030	3, 1.5		36			
REF2033	3.3, 1.65		39.6			
REF2041	4.096, 2.048		49.2			
REF3012	1.25	50 <sup>(1)</sup> , 60 <sup>(2)</sup> , 65 <sup>(3)</sup> , 75 <sup>(4)</sup>	14	0.2	42	25
REF3020	2.048		23			
REF3025	2.5		28			
REF3030	3		33			
REF3033	3.3		36			
REF3040	4.096		45			
REF3112	1.25	15 <sup>(1)</sup> , 20 <sup>(4)</sup>	17	0.2	100	10
REF3120	2.048		27			
REF3125	2.5		33			
REF3130	3		39			
REF3133	3.3		43			
REF3140	4.096		53			
REF3312	1.25	30 <sup>(4)</sup>	35	0.15	3.9	5
REF3318	1.8		50			
REF3320	2.048		55			
REF3325	2.5		70			
REF3330	3		84			
REF3333	3.3		92			
REF3425	2.5	6 <sup>(4)</sup>	12.5	0.05	72	10
REF3430	3		15			
REF3433	3.3		16.5			
REF3440	4.096		20.5			
REF3450	5		25			

(1) NOTE: This specification applies in the 0 to 70 °C Operating temperature range range.

(2) NOTE: This specification applies in the -30 to 85 °C Operating temperature range range.

(3) NOTE: This specification applies in the -40 to 85 °C Operating temperature range range.

(4) NOTE: This specification applies in the -40 to 125 °C Operating temperature range range.

# Precision voltage references

Part number	Voltage output V	Temp drift (max) ppm/°C	1/f Noise 0.1 to 10 Hz (typ) μVp-p	Initial accuracy (max) %	Quiescent current (typ) μA	Output current mA
REF5010	10	3 <sup>(4)</sup>	30	0.05	800	10
REF5020	2.048		6			
REF5025	2.5		7.5			
REF5030	3		9			
REF5040	4.096		12			
REF5045	4.5		13.5			
REF5050	5		15			
REF6025	2.5	5 <sup>(4)</sup>	7.5	0.05	820	4
REF6030	3		9		820	
REF6033	3.3		9.9		820	
REF6041	4.096		12.3		820	
REF6045	4.5		13.5		830	
REF6050	5		15		830	
REF6125	2.5	8 <sup>(4)</sup>	7.5	0.05	820	4
REF6130	3		9		820	
REF6133	3.3		9.9		820	
REF6141	4.096		12.3		820	
REF6145	4.5		13.5		830	
REF6150	5		15		830	
REF6225	2.5	3 <sup>(1)</sup>	7.5	0.05	820	4
REF6230	3		9		820	
REF6233	3.3		9.9		820	
REF6241	4.096		12.3		820	
REF6245	4.5		13.5		830	
REF6250	5		15		830	

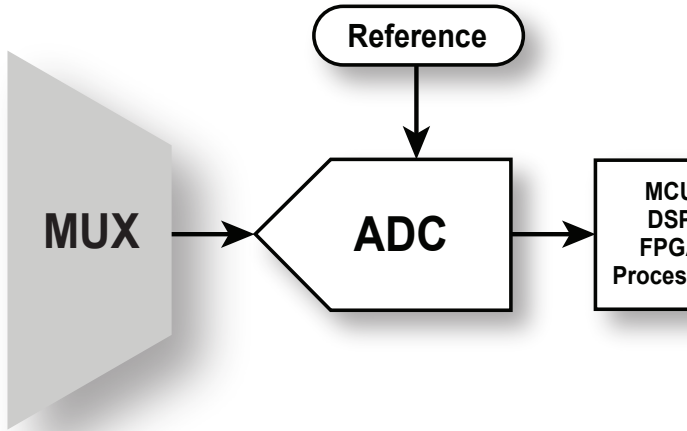
(1) NOTE: This specification applies in the 0 to 70 °C Operating temperature range range.

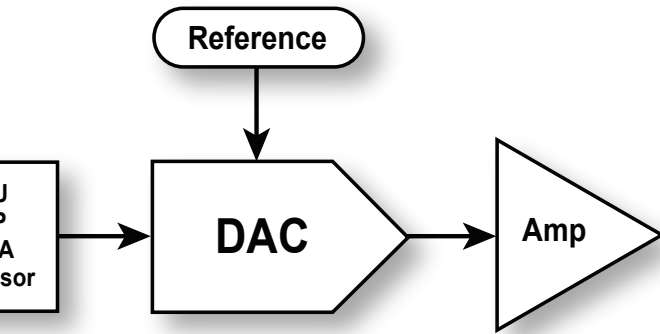
(2) NOTE: This specification applies in the -30 to 85 °C Operating temperature range range.

(3) NOTE: This specification applies in the -40 to 85 °C Operating temperature range range.

(4) NOTE: This specification applies in the -40 to 125 °C Operating temperature range range.

# Multiplexers





# Multiplexers

## Supply voltage:

The supply voltage is the recommended voltage range that can be passed through the multiplexer (mux). This can range can be symmetric (e.g. -18V to 18V or 0V to 36V) or asymmetric (e.g. -6V to 30V), which simplifies the circuit design while providing flexibility for the signals that are being multiplexed.

## On-resistance ( $R_{on}$ ):

The on-resistance is the resistance between the drain and source terminal (input and output) of the mux. This resistance contributes to the signal distortion (gain error) of the data acquisition system, depending on the impedance at the output of the mux (with higher leakage resulting in a higher error). A buffer amplifier with high impedance can be added to the output of the mux to minimize the distortion created.

In addition, this resistance contributes to the cutoff frequency of low-pass filter which is created by the mux's internal resistance in combination with the on-capacitance. A lower resistance decreases the cutoff frequency of the filter.

## Leakage current ( $I_{s(off)}$ , $I_{d(off)}$ , $I_{d(on)}$ ):

Off-leakage current is composed of the source and drain off-leakage current, and is the current flowing through the source or drain pin when a channel is in the off state.

On-leakage current is the current that flows through the mux drain pin when a channel is in the on state.

Together on and off-leakage currents contribute to the DC errors of a data acquisition system, thus the lower the leakage current the more accurate the system will be.

## On-capacitance ( $C_{on}$ ):

On-capacitance is the combination of the source and drain capacitances. A low on-capacitance enables a fast settling time required for high precision systems. A low on-capacitance decreases the cutoff frequency of the low pass filter which is created by the mux's internal resistance in combination with the on-capacitance.

## Charge injection:

Charge injection is a voltage change introduced at the output of a mux when it is turned on or off. A low charge injection reduces the distortion created when a channel is connected or disconnected, thus enabling faster switching between channels of a mux.

## Operating temperature range:

The operating temperature range spec of a mux is the recommended operating temperature range for the device.



Part number	Supply voltage (V)	On-resistance ( $\Omega$ )	$I_{s(off)}$ (nA)	$I_{d(off)}$ (nA)	$I_{d(on)}$ (nA)	$C_{s(off)}$ (pF)	$C_{on}$ (pF)	Charge injection (pC)(typ)	Operating temperature range ( $^{\circ}$ C)
<b>MUX36S08</b>	36	170	0.04	0.1	0.1	2.9	10.6	0.15	-40 to 125
<b>MUX36D04</b>	36	170	0.04	0.1	0.1	2.9	7.7	0.15	-40 to 125
<b>MUX508</b>	36	170	1	1	1	2.9	10.6	0.15	-40 to 125
<b>MUX509</b>	36	170	1	1	1	2.9	7.7	0.15	-40 to 125

**\*All values are the maximum value at 25 $^{\circ}$ C when using a dual supply, unless otherwise noted**

**\*\*Refer to product datasheet for full device specifications**

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