maXimator

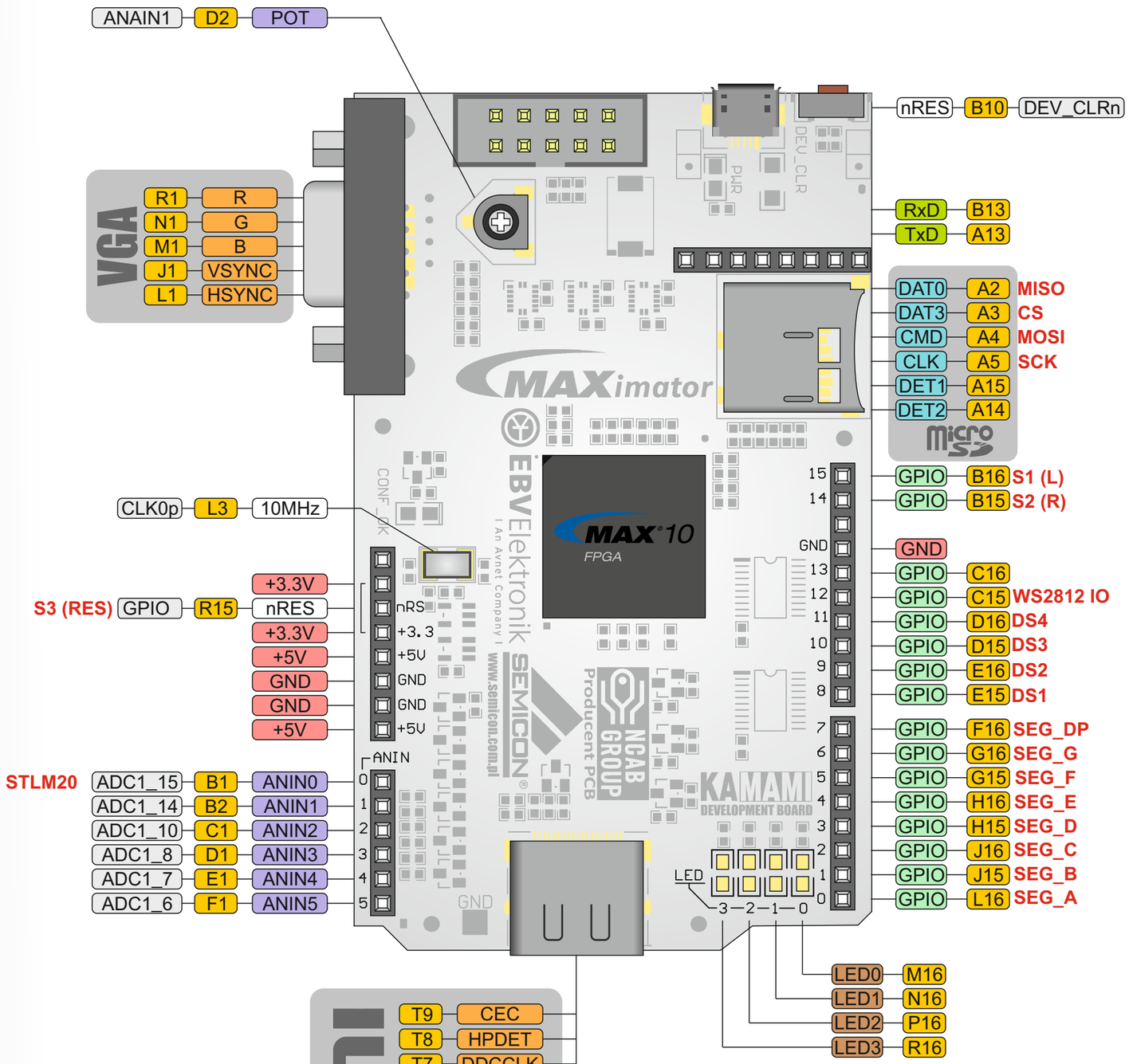
Developed and produced by

KAMAMI

Partners

NCAB GROUP
PCB manufacturer

http://maximator-fpga.org



Comments

1. All GPIOs are bidirectional and 3.3V/5V logic compatible
2. Range of acceptable voltage on all ADC inputs is 0...+2.5V
3. Analog input ADC1_8 (ANIN3) has optional internal voltage divider 1:2V/V
4. MicroSD card interface is 3.3V logic compatible
5. nRES input is 3,3V and 5V logic compatible
6. Red marked signals are power supply

Doc version changes:

- ANIN5 was connected with ADC1_5 ch/is connected with ADC1_6 ch
- nRES removed from B10 pin (DEV_CLRn)
- Peripheral connections addendum

ver. 1.4